



Power-up Sequence:
(Power-down sequence is reverse of power-up sequence)

- 0 5V/1p8V_PRE/3.3V_STBY/12V
 - 1 EN1: 2.5V/3.3V
DDR4_VCCP
DDR4_VCCSPD
QSFP1,2_VCC
QSFP1,2_VCCT
QSFP1,2_VCCR
 - 2 EN2: S10_VCC/VCCP
 - 3 EN3: S10_VCCERAM
VCCPLLDIG_SDM
EN4: S10_VCCRR_GXB
S10_VCCRL_GXBL
EN5: S10_VCCCT_GXBRL
 - 4 EN6: VCCPT
VCCBAT
VCCIO_SDM
VCCIO_1.8V
VCCCH_GXB
VCCA_PLL
VCCPLL_SDM
VCCADC
 - 5 EN7: VCCM
 - 7 EN8: VCCIO_UIB
 - 8 EN9: VCCIO_1.2_DDR4
- DDR4_VTT, DDR4_VREF
VCCIO_1.2_DDR4, DDR4_VDD
HILO VDD/VDDQ

Note 1. EV1320, 2A DDR terminator is cross reference Intel Enpirion Part.