Intel® Server System M50CYP1UR

System Integration and Service Guide

A guide providing instructions for the insertion and extraction of system components and available Intel accessories and spares

Rev. 1.4
April 2022

Delivering Breakthrough Data Center System Innovation – Experience What's Inside!
## Document Revision History

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Safety Warnings

Heed safety instructions: Before working with your server product, whether you are using this guide or any other resource as a reference, pay close attention to the safety instructions. You must adhere to the assembly instructions in this guide to ensure and maintain compliance with existing product certifications and approvals. Use only the described, regulated components specified in this guide. Use of other products/components will void the UL listing and other regulatory approvals of the product and will most likely result in noncompliance with product regulations in the region(s) in which the product is sold.

System power on/off: The power button DOES NOT turn off the system AC power. To remove power from the system, you must unplug the AC power cord. Make sure the AC power cord is unplugged before you open the chassis, add, or remove any components.

Hazardous conditions, devices and cables: Hazardous electrical conditions may be present on power, telephone, and communication cables. Turn off the server and disconnect the power cord, telecommunications systems, networks, and modems attached to the server before opening it. Otherwise, personal injury or equipment damage can result.

Installing or removing jumpers: A jumper is a small plastic encased conductor that slips over two jumper pins. Some jumpers have a small tab on top that you can grip with your fingertips or with a pair of fine needle nosed pliers. If your jumpers do not have such a tab, take care when using needle nosed pliers to remove or install a jumper; grip the narrow sides of the jumper with the pliers, never the wide sides. Gripping the wide sides can damage the contacts inside the jumper, causing intermittent problems with the function controlled by that jumper. Take care to grip with, but not squeeze, the pliers or other tool you use to remove a jumper, or you may bend or break the pins on the board.

Electrostatic Discharge (ESD)

Electrostatic discharge can damage the computer or the components within it. ESD can occur without the user feeling a shock while working inside the system chassis or while improperly handling electronic devices like processors, memory or other storage devices, and add-in cards.

Intel recommends that the following steps be taken when performing any procedures described within this document or while performing service to any computer system.

- Where available, all system integration and/or service should be performed at a properly equipped ESD workstation
- Wear ESD protective gear like a grounded antistatic wrist strap, sole grounders, and/or conductive shoes
- Wear an anti-static smock or gown to cover any clothing that may generate an electrostatic charge
- Remove all jewelry
- Disconnect all power cables and cords attached to the server before performing any integration or service
- Touch any unpainted metal surface of the chassis before performing any integration or service
- Hold all circuit boards and other electronic components by their edges only
- After removing electronic devices from the system or from their protective packaging, place them component side up on to a grounded anti-static surface or conductive workbench pad. Do not place electronic devices on to the outside of any protective packaging.
Caution: Slide / Rail mounted equipment is not to be used as a shelf or a workspace.

Intel warrants that this product will perform to its published specifications. However, all computer systems are inherently subject to unpredictable system behavior under various environmental and other conditions. This product is not intended to be the sole source for any critical data and the user must maintain a verified backup. Failure to do so or to comply with other user notices in the product user guide and specification documents may result in loss of or access to data.

Weight of the system:

- Due to the weight of a system, Intel recommends carrying the system with two people supporting the system from the sides or using a mechanical lift or a cart when moving the system from one location to another.
- If your system has rack handles installed, do not lift or carry the system by the rack handles
- When lifting or moving a chassis, always grasp it by all four corners. Do not grasp the chassis by two points at opposing diagonal corners, doing so may damage the internal components.
- If you can only grasp the chassis at two different points, always grasp the chassis by the sides at the midpoint.
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1. **Introduction**

The Intel® Server System M50CYP1UR is a purpose-built rack mount server that delivers power and performance within a 1U form factor. The system supports up to two 3rd Gen Intel® Xeon® Scalable processors, delivering high core count and new hardware-enhanced security features. Previous generation Intel Xeon processor and Intel Xeon Scalable processor families are not supported.

The server system supports up to 32 DDR4 DIMMs, providing high memory bandwidth for memory intensive workloads. The product family supports Intel® Optane™ persistent memory 200 series modules.

For details on all system features, refer to the *Intel® Server System M50CYP1UR Technical Product Specification (TPS)*. See Section 1.1 for a complete list of available product documentation.
About This Document

This document provides system integrators and service technicians with instructions for the installation and removal of system components. The document also covers available Intel accessories supported by this server system.

The document is organized into two sections. The first section (Chapters 2 through 4) is focused on the installation of system components and accessories into an L6 or L9 integrated server system.

The second section (Chapters 5, 6, and appendices A-G) is focused on system service. The section provides the service technician with valuable system information and procedures necessary to successfully identify and replace a faulty system component.

System Integration

Chapter 2, L6 System Integration – Essential System Components – Installation procedures for the following system components: Processors, Memory, and Power Supply. Removal and installation of the system top cover are covered.

Chapter 3, L9 System Integration - Provides detailed instructions necessary to enhance system configurations by installing additional components and/or available accessory kits.

Chapter 4, System Software Update and Configuration – A short overview describing the system software stack installed on new Intel servers and where to get the latest revisions.

System Service

Chapter 5, System Service – System Features Overview. An overview that identifies and locates the features associated with the Intel® Server System M50CYP1U.

Chapter 6, System Service and FRU Replacement. Installation procedures for system field replaceable units (FRUs).

Appendix A, Getting Help. Provides server system support and contact information.

Appendix B, Internal Cable Routing Channels. Provides cable routing guidance.


Appendix D, System Status LED Operating States and Definition. System status LED operating states and definition.

Appendix E, POST Code Diagnostic LED Decoder. Lists of Diagnostic LED codes.

Appendix F, POST Code Errors. List of POST code errors that represent specific failures, warnings, or information.

Appendix G, System Packaging Assembly Instructions. Provides detailed instructions to repack the server system into the original Intel packaging.

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1 An L6 integrated system requires essential components to be installed to make it power-on ready. An L9 integrated system is power-on ready but may require additional options and/or accessories to be installed to enable specific system features.
1.1 Reference Documents and Support Collaterals

For additional information, see the product support collaterals specified in the following table. The following webpage provides support information for the Intel® Server M50CYP Family: https://www.intel.com/content/www/us/en/support/products/200321.html

Table 1. Intel® Server M50CYP Family Reference Documents and Support Collaterals

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<td>System integration instructions and service guidance</td>
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<td>3rd Generation Intel® Xeon® Scalable Processor, Codename Ice Lake-SP and Cooper Lake-SP - Thermal and Mechanical Specifications and Design Guide: Document ID 574080</td>
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Note: Intel Confidential documents are made available under a Non-Disclosure Agreement (NDA) with Intel and must be ordered through your local Intel representative.
2. L6 Integrated System – Essential System Component Installation

The Intel® Server System M50CYP1UR options are offered with different level of system integration. System configurations that are not power-on ready are identified as L6 integrated systems. An L6 integrated system requires essential components (sold separately) to be installed. If your Intel system did not come pre-installed with any of the following components, then follow the procedures in this chapter:

- 1 (Required) or 2 (Optional) processors – 3rd Gen Intel® Xeon® Scalable processor family
- Memory – Up to 32 DDR4 DIMMs
- 1 (Required) or 2 (Optional) power supplies

If your Intel server system came pre-installed with all the components listed above, then skip this chapter and go to Chapter 3 for installation procedures associated with all other system options and accessories.

Before You Begin

Before integration of any system components, review all the safety and ESD precautions found in the Safety Warnings section at the beginning of this document.

System Reference

In the following procedures, all references to left, right, front, top, and bottom assume the reader is facing the front of the server chassis.

Instruction Format

Each procedure described in this chapter follows an illustration first format. This format gives the reader the option to follow a quicker path to component integration by first seeing an illustration of the intended procedure. If necessary, the reader can then follow the step-by-step instructions that accompany each procedure.
2.1 Chassis Component Identification

The following figure shows the chassis components.

![Figure 1. Chassis Component Identification](image)

2.2 System Cover Removal / Installation

The system top cover consists of two panels – one over the front half of the system and one over the back half of the system. To maintain system thermals, both top cover panels must always be in place when the system is operating.

**Required Tools and Supplies**

- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips* head screwdriver #1

2.2.1 System Cover Removal

Removal of both top cover panels is necessary when installing or replacing any system component within the server chassis. Before removing the top cover, power down the system, and unplug all peripheral devices and the power cable(s).
The system ships from the factory with the front system cover panel and back system cover panel screwed to the chassis. A total of four screws, one on each side of the front panel and one on each side of the back panel, need to be removed to detach both top cover panels from the chassis.

**Note:** A non-skid surface or a stop behind the server system may be needed to prevent the server system from sliding on the work surface.

For each top cover panel:

1. While pushing down on both the left and right buttons of the given top panel (see Letter A), slide the top cover panel towards the front (front panel) or back (back panel) of the chassis (see Letter B).
2. Carefully lift the top cover panel up and away from the chassis.

**Note:** Each top cover panel can slide along the chassis base for 10 mm and then needs to be lifted.
2.2.2 System Cover Installation

For each top cover panel:

1. Carefully align and set the top cover panel on top of the chassis. Then, slide it inwards until it locks into place (see Letter A).

Shipping Note: When transporting the server system, Intel recommends installing the four top cover screws before shipping.

2.3 Processor Assembly and Installation

Components Required:
- 3rd Gen Intel Xeon Scalable processors
- Processor carrier clip
- 1U standard heat sink or 1U Enhanced Volume Air Cooling (EVAC) heat sink
- Processor tray (comes with the processor)

Required Tools and Supplies
- Anti-static wrist strap and conductive workbench pad (recommended)
- T-30 Torx* screwdriver
- ESD Gloves
- Phillips* head screwdriver #2

The Intel® Server System M50CYP1UR includes two Socket-P4 LGA4189 processor sockets compatible with the 3rd Gen Intel Xeon Scalable processor family.

Two types of heat sinks are supported as shown in the following figure: Standard 1U heat sink and Enhanced Volume Air Cooling (EVAC) heat sink. The type of heat sink used depends on the system thermal requirements. The 2.5" x4 front drive system must use the EVAC heat sink on both processors. The 2.5" x12 front drive system must use the standard 1U heat sink on both processors. For more information, refer to the Intel® Server System M50CYP1UR Technical Product Specification (TPS) and Intel® Server M50CYP Family Configuration Guide.
A processor heat sink module (PHM) assembly and processor socket assembly are necessary to install a processor to the server board. Figure 6 identifies each component associated with the PHM and processor socket assemblies.

**Caution:** Fin edges of the processor heat sink are very sharp. Intel recommends wearing thin ESD protective gloves when handling the PHM during the following procedures.

**Note:** The following sections show the EVAC heat sink in the figures, but the procedures described apply to both heat sink types.
Figure 6. Processor Heat Sink Module (PHM) Reference Diagram

To properly assemble the PHM and install it to the server board, the procedures described in the following sections must be followed in the order specified. These instructions assume that all the PHM components are new and the Thermal Interface Material (TIM) is already applied to the bottom of the heat sink.

**Note:** Heat sinks are shipped pre-installed with TIM type Honeywell® PTM7000.

### 2.3.1 Processor Heat Sink Module (PHM) Assembly

**Note:** The label on the heat sink refers to PHM installation onto the server board. It does not refer to the PHM assembly process.
1. Place the processor carrier clip on top of the processor while it is still on the tray.
2. Ensure that the Pin 1 indicator on the processor carrier clip is aligned with the Pin 1 indicator of the processor.

3. Gently press down on two opposite sides at a time of the processor carrier clip until it clicks.
4. Remove the heat sink from its packaging. To avoid damage to the heat sink, grasp it by its narrower top and bottom edges.

5. Set the anti-tilt wires to the outward position.
6. Turn the heat sink over and place it bottom side up on a flat surface.
7. Remove the plastic protective film from the Thermal Interface Material (TIM).
8. Align Pin 1 indicator of processor carrier clip with the corner cut-out on the heat sink. For the EVAC heat sink, align the processor carrier clip and the heat sink as shown in the above figure.

**Note:** In a standard heat sink there are two cut-out corners, either can be used to align Pin 1 indicators.

9. Gently press down the heat sink onto the processor carrier clip until it clicks into place.
10. Ensure that all four heat sink corners are securely latched to the processor carrier clip tabs.

### 2.3.2 Processor Installation

**Caution:** Do not touch the socket pins. The pins inside the processor socket are extremely sensitive. A damaged processor socket may produce unpredictable system errors.

---

1. Remove the protective cover by squeezing the finger grips (see Letter A) and pulling the cover up (see Letter B).
2. Ensure that the socket is free of damage or contamination before installing the PHM.
Caution: If debris is observed, blow it away gently. Do not remove it manually, such as with tweezers.

Figure 13. PHM Alignment with Socket Assembly

3. Align the Pin 1 indicators of the processor carrier clip and processor with the Pin 1 indicator on the bolster plate.

Caution: Processor socket pins are delicate and bend easily. Use extreme care when placing the PHM onto the processor socket. Do not drop it.

Figure 14. PHM Installation onto Server Board

4. Set all four anti-tilt wires on the heat sink to the inward position (see Letter A).
5. Holding the PHM horizontally, carefully lower it on to the bolster plate's alignment pins (see Letter B).
6. Set all four anti-tilt wires on the heat sink to the outward position (see Letter D).
7. Using a Phillips #2 screwdriver, tighten the heat sink extension screws (see Letter C).

![Figure 15. Tighten Heat Sink Extension Fasteners](image)

8. Tighten the heat sink fasteners using a T30 Torx* screwdriver to 8 in-lb. No specific sequence is needed for tightening.

**Important:** Do not install a processor heat sink on an empty socket. Also, only install a socket cover on an empty socket.

### 2.4 Memory Module Installation

**Required Tools and Supplies**

- Anti-static wrist strap and conductive workbench pad (recommended)

The Intel® Server Board M50CYP2SB supports DDR4 standard RDIMMs, 3DS-RDIMMs, Load Reduced DDR4 RDIMMs (LRDIMMs), and 3DS-LRDIMMs. In addition, the server board supports Intel® Optane™ persistent memory 200 series modules (also known as, Intel Optane PMem). Only standard DDR4 DIMMs are shown in the following figures, but the steps of DDR4 DIMM installation and replacement are the same for standard DDR4 DIMMs and Intel Optane PMem 200 series modules.

DDR4 DIMM and Intel Optane PMem are commonly referred to as "memory module" in the following instructions.

**Note:** See Appendix C for general memory population rules.
Figure 16. DIMM Blank Removal

1. Remove the DIMM blank from the desired memory slot
   - Open the ejection tabs at both ends of the selected memory slot to lift the DIMM blank from the slot (see Letter A).
   - Carefully remove the DIMM Blank from the system (see Letter B).

Figure 17. Memory Module Installation

2. Ensure that the memory module ejection tabs at both ends of the DIMM slot are pushed outward to the open position (see Letter A).
3. Carefully unpack the replacement memory module, taking care to only handle the device by its outer edges.
4. Align the notch at the bottom edge of the memory module with the key in the DIMM slot (see Letter B).
5. Insert the Memory module into the slot (see Letter C).
   a. Using even pressure along the top edge, push down on the memory module (see Letter C) until the ejection tabs of the memory slot snap into place (see Letter D).
6. Ensure that the ejection tabs are firmly in place (see Letter E).

Note: Intel Optane PMem 200 series modules require additional steps to enable and configure them. Refer to the appropriate Intel Optane PMem documentation to complete the installation process.
2.5 Power Supply Module Installation

Required Tools and Supplies

- Anti-static wrist strap and conductive workbench pad (recommended)

![Figure 18. Power Supply Installation](image)

1. If present, remove the insert from the power supply bay (see Letter A).
2. Slide the power supply into the power supply bay until it clicks and locks in place (see Letter B).

**Note:** A single power supply configuration requires that the power supply bay insert be installed on the unused bay when the system is operational.

3. (If using the second power supply) Remove the insert from the second power supply bay.
4. (If using the second power supply) Slide the power supply into the second power supply bay until it clicks and locks in place.
3. **System Options / Accessory Kit Installation**

This chapter provides instructions for the integration of system options and other Intel accessories. If your integrated Intel server system did not come pre-installed with processors, memory, or power supplies, installation procedures for these components are in Chapter 2.

**Before You Begin**

Before integration of any system components, review all the safety and ESD precautions found in the Safety Warnings section at the beginning of this document.

**System Reference**

In the following procedures, all references to left, right, front, top, and bottom assume that the reader is facing the front of the server chassis.

**Instruction Format**

Each procedure described in this chapter follows an illustration first format. This format gives the reader the option to follow a quicker path to component integration by first seeing an illustration of the intended procedure. If necessary, the reader can then follow the step-by-step instructions that accompany each procedure.
3.1 System Cover Removal / Installation

The system top cover consists of two panels – one over the front half of the system and one over the back half of the system. To maintain system thermals, both top cover panels must always be in place when the system is operating.

Required Tools and Supplies

- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips* head screwdriver #1

3.1.1 System Cover Removal

Removal of both top cover panels is necessary when installing or replacing any system component within the server chassis. Before removing the top cover, power down the system and unplug all peripheral devices and the power cable(s).

![Figure 19. System Top Cover Panel Shipping Screws](image)

The system ships from the factory with the front system cover panel and back system cover panel screwed to the chassis. A total of four screws, one on each side of the front panel and one on each side of the back panel, need to be removed to detach both top cover panels from the chassis.

**Note:** A non-skid surface or a stop behind the server system may be needed to prevent the server system from sliding on the work surface.
For each top cover panel:

1. While pushing down on both the left and right buttons of the given top panel (see Letter A), slide the top cover panel towards the front (front panel) or back (back panel) of the chassis (see Letter B).
2. Carefully lift the top cover panel up and away from the chassis.

**Note:** Each top cover panel can slide along the chassis base for 10 mm and then needs to be lifted.

### 3.1.2 System Cover Installation

For each top cover panel:

1. Carefully align and set the top cover panel on top of the chassis. Then, slide it inwards until it locks into place (see Letter A).

**Shipping Note:** When transporting the server system, Intel recommends installing the four top cover screws before shipping.
3.2 Riser Card / Add-in Card Installation

The Intel® Server System M50CYP1UR supports various riser card options. Depending on the system configuration, your system may or may not come pre-configured with riser card options installed. This section provides assembly and installation instructions for systems that require riser card installation.

Available Riser Card options:

Riser Slot #1 supports the following Intel Riser Card option:
- One PCIe slot riser card supporting
  One LP/HL, single-width slot (x16 electrical, x16 mechanical) iPC – CYP1URISER1STD

Riser Slot #2 supports the following Intel Riser Card options:
- One PCIe slot riser card supporting
  One LP/HL, single-width slot (x16 electrical, x16 mechanical) iPC – CYP1URISER2STD
- NVMe riser card supporting
  One LP/HL, single-width slot (x16 electrical, x16 mechanical) +
  One x8 PCIe NVMe* SlimSAS* connector with re-timer. Included in iPC – CYP1URISER2KIT

PCIe* Interposer Riser Slot (requires PCIe* SlimSAS* riser card in Riser Slot #2):
- PCIe Interposer Riser Slot supports the PCIe interposer riser card as an accessory option. This card supports one PCIe add-in card (x8 electrical, x8 mechanical). The PCIe interposer riser card can be used only when it is connected to the PCIe riser card in Riser Slot #2. The interposer card uses x8 PCIe data lanes routed from the PCIe SlimSAS connector on the PCIe riser card. The Intel accessory kit includes the PCIe interposer riser card, PCIe SlimSAS riser card for Riser Slot #2, and PCIe interposer cable. iPC – CYP1URISER2KIT

Riser Slot #3 supports the following Intel Riser Card option:
- NVMe riser card supporting
  Two PCIe NVMe SlimSAS connectors iPC – CYPRISER3RTM

All system configurations include the mounting brackets for each supported riser card option.
Figure 23. Bracket for Riser Card on Riser Slot #2 – Two Views

Figure 24. Bracket for Riser Card on Riser Slot #3 – Two Views (Support for NVMe* Riser Only)

Figure 25. Bracket for PCIe* Interposer Riser Card – Two Views

**Required Tools and Supplies**

- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips* head screwdriver #1

For more information about the riser card options and supported add-in cards, see the *Intel® Server System M50CYP1UR Technical Product Specification* and the *Intel® Server M50CYP Family Configuration Guide* for ordering information.
3.2.1  Riser Card Bracket Removal
As shown in the figures above, there are multiple types of riser card brackets included with the system. The instructions for installation are the same for each.

![Figure 26. Riser Card Bracket Removal](image)

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1).
3. If present, disconnect all cables (internal or external) that may be attached to the riser assembly.
4. Loosen the two screws on the side of the riser card bracket (see Letter A).
5. Grasp the riser card bracket with both hands and carefully pull it up and away from the chassis. (see Letter B).
6. Reinstall the system top cover (see Section 3.1.2).

3.2.2  Riser Card Assembly Installation
The following installation instructions for riser card to bracket are the same for all included brackets and supported riser card options.

![Figure 27. Riser Card Installation onto the Bracket](image)

1. Align and mount the riser card to the bracket using the mounting holes.
2. Using the fastener screws, secure the riser card to the bracket. Tighten to 5 in-lb.
3.2.3 PCIe® Add-in Card Installation

The add-in card connected to the PCIe interposer riser card or the riser card in Riser Slot #1 must be oriented with component side up as shown in the following figure. The add-in card connected to the riser card in Riser Slot #2 must be oriented with component side down as shown in Figure 29.

Figure 28. PCIe® Add-in Card Installation for Riser Card on Riser Slot #1 or PCIe® Interposer Riser Card
Figure 29. PCIe* Add-In Card Installation for Riser Card on Riser Slot #2

**Note:** Riser Slot #3 is only used to provide an additional PCIe NVMe interface to the hot-swap backplane mounted to the front drive bay.

1. If the riser card assembly (bracket and board) is still inside the system, remove it from the system following instructions in Section 3.2.1.
2. Remove the fastener screw (see Letter A) and remove the filler panel from the add-in card slot (see Letter B).
3. Insert the add-in card until it is fully seated inside the PCIe slot on the riser card (see Letter C).
4. Using the fastener screws, secure the add-in card to the riser card assembly (see Letter D). Tighten to 5 in-lb.

**Note:** For add-in cards with internal cable connectors, it may be necessary to connect cable(s) before installing the riser card assembly into the system. See Appendix B for cable routing guidance.
3.2.4 Add-in Card Assembly Installation – Riser Slot #1, Riser Slot #2, and PCIe* Interposer Riser Slot

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1).
3. Position the add-in card assembly's edge connector over the riser slot on the server board.
4. Align the two key slots on the back edge of the riser card assembly with the mounting keys on the back of the chassis.
5. Once aligned, press the riser card assembly straight down into the riser slot (see Letter A).
6. Using the fastener screws, secure the riser card assembly to the system (see Letter B). Tighten to 5 in-lb.
7. Connect any cables to the add-in card that are required. See your add-in card documentation for additional information.
8. Reinstall the system top cover (see Section 3.1.2).

3.3 Front Drive Installation

The Intel® Server System M50CYP1UR has front drive bay chassis options that support 2.5" form factor drives (SSDs only).

In 2.5" front drive bay systems, 7 mm thick SSDs are supported when used with the supplied drive blank. The 2.5" drive bay options also support 15 mm drives. Each drive is mounted to a tool-less, non-detachable, mounting rail and interfaces with a backplane. The front drives are hot swappable.

This section provides instructions for drive assembly, drive installation into the chassis, and drive removal from the chassis. Figure 31 identifies the drive bay components.

Required Tools and Supplies

- Anti-static wrist strap and conductive workbench pad (recommended)
**Figure 31. 2.5" Drive Bay Components**

**Note:** To ensure proper system airflow requirements, drive mounting rails must be populated with either a drive or supplied drive blank.

**Note:** The 2.5" drive mounting rails in the system are not removable. They slide out so that the storage drives can be installed or removed in/from them. When sliding out a drive mounting rail from the system, only pull it as much as it allows without forcing it.

### 3.3.1 SSD Drive Blank Removal

The 2.5" drive mounting rails in the system are not removable. They slide out so that the storage drives can be installed or removed. When sliding out a drive mounting rail from the system, only pull it as much as it allows without forcing it.

**Figure 32. 2.5" 7 mm Drive Removal**
1. Press the button on the drive extraction lever to release it (see Letter A).
2. Using the lever, pull the drive mounting rail part way out of the drive bay (see Letter B).
3. Remove the drive blank from the drive mounting rail (see Letter C).

### 3.3.2 2.5" SSD Drive Assembly for 7 mm Drives

The server system supports 2.5" SSDs with 7 mm of thickness when used with the supplied blanks. The supplied blank for 2.5" bays has two parts: top and bottom. The top part must be attached to the 7 mm drive to fit properly inside the 2.5" bay. This section provides instructions to attach the supplied blank part to a 7 mm thick SSD.

1. Press the handles at the bottom part of the drive blank (see Letter A).
2. Separate the top and bottom parts while pressing the handles (see Letter B).
3. Insert the 2.5-inch 7 mm SSD drive into the top part of the drive blank as shown in the above figure.

**Important:** To avoid damaging the SSD connector, insert the drive into the supplied blank in the direction shown.

With the supplied blank attached to the drive, it can be installed as a regular 2.5" SSD drive.

### 3.3.3 2.5" SSD Drive Installation

**Figure 36. 2.5" 7 mm Drive Outside Chassis, Ready for Installation**
1. Ensure that the drive extraction lever is in the open position and the drive mounting rail is pulled out halfway.
2. Align the drive or drive assembly with the open drive bay.
3. Insert the drive or drive assembly into the drive bay (see Letter A).

   **Note:** Intel recommends holding the drive with one hand while holding the lever with the other hand.

4. Slide the drive forward until it is fully connected to the backplane (see Letter B).
5. Complete the drive installation by closing the drive extraction lever until it locks into place (see Letter C).
3.4 Ethernet Network Adapter for OCP* – Installation

This section provides instructions for OCP adapter installation.

Required Tools and Supplies

- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips* head screwdriver #1

To broaden the standard onboard feature set, the server system supports the Intel® Ethernet Network Adapters indicated in the following table. These adapters are compatible with the Open Compute Project (OCP) 3.0 Specification.

Note: Only the Ethernet Network Adapters for OCP listed in the following table are supported.

All OCP adapter types support one of three engagement mechanisms: pull tab, ejector latch, and internal lock. The engagement mechanism refers to the mechanism required to install/remove the OCP adapter. Instructions for OCP adapter installation and removal for all three configurations are provided in the following subsections.

Table 2. Supported Intel® Ethernet Network Adapters for OCP*

<table>
<thead>
<tr>
<th>Description</th>
<th>Interface</th>
<th>iPC</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dual port, RJ45, 10/1 GbE</td>
<td>PCIe 3.0</td>
<td>X710T2LOCPV3</td>
</tr>
<tr>
<td>Quad port, SFP+ DA, 4x 10 GbE</td>
<td>PCIe 3.0</td>
<td>X710DA4OCPV3</td>
</tr>
<tr>
<td>Dual Port, QSFP28 100/50/25/10 GbE</td>
<td>PCIe 4.0</td>
<td>E810CQDA2OCPV3</td>
</tr>
<tr>
<td>Dual Port, SFP28 25/10 GbE</td>
<td>PCIe 4.0</td>
<td>E810XXVDA2OCPV3</td>
</tr>
</tbody>
</table>

For more information on the supported OCP compatible Ethernet Network Adapters, see the Intel® Server System M50CYP1UR Technical Product Specification.

3.4.1 OCP* Adapter Bay Filler Removal / Installation

The OCP adapter bay has a filler that needs to be removed before installing any of the OCP adapter configurations.

Caution: If the OCP adapter is removed and the bay becomes empty again, this filler needs to be reinstalled.

The following steps show how to remove and install OCP adapter bay filler.
To remove the filler:

1. Loosen the thumb screw on the right side of the filler (see Letter A).
2. Pull out the right side of the filler first and move the filler slightly to the right, ensuring the left end of the filler is out of the slot.
3. Pull the filler away from the chassis (see Letter B).

To install the filler:

1. Insert the left side of the filler on the slot.
2. Push the filler until the mounting screw goes into the mounting hole on the right side (see Letter A).
3. Tighten the thumb screw (see Letter B).
3.4.2 OCP* Adapter with Pull Tab Installation

![Figure 42. OCP* Adapter with Pull Tab Installation](image)

1. Align the OCP adapter with the open OCP bay slot and slide forward until the connectors make secure contact (see Letter A).
2. Tighten the thumb screw on the right side of the OCP adapter (see Letter B).

3.4.3 OCP* Adapter with Ejector Latch Installation

![Figure 43. OCP* Adapter with Ejector Latch Installation](image)

1. Ensure that the latch is in the open position.
2. Push the OCP adapter forward until the right side of the latch contacts the rear panel (see Letter A).
3. Rotate the latch towards the inside to get the OCP adapter engaged with the connectors and close the latch (see Letter B).

3.4.4 OCP* Adapter with Internal Lock Installation

All L6 and L9 integrated systems come with an internal lock on the OCP rail. This lock is a piece of blue plastic. The OCP rail in the system has a dedicated space to accommodate the lock. The lock can be mounted on the rail in two different orientations. When the keying features of the lock are facing up, it is in an unlocked orientation. When the keying features are facing down, it is in a locked orientation. A lock symbol is included in each side of the plastic lock to indicate its orientation. The following figure shows the features of the lock.
1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1)
3. Remove any riser card assembly above the OCP adapter area, if present (see Section 3.2.1).

**Note:** In the default shipping configuration, the internal lock is set to the unlock orientation.

4. Squeeze the two hooks of the internal lock and pull it out (see Letter A).
5. Align the OCP adapter with the open OCP bay slot and slide forward until the connectors make secure contact (see Letter B).
6. Reinstall the internal lock with the lock orientation (see Letter C).
7. Reinstall the riser card assembly as needed (see Section 3.2.4).
8. Reinstall the system top cover (see Section 3.1.2).
3.5 M.2 Storage Device Installation

The server board includes two M.2 connectors as shown in the following figure. Each M.2 connector supports a PCIe NVMe or SATA SSD drive that conforms to a 22110 (110 mm) or 2280 (80 mm) form factor.

Required Tools and Supplies

- M.2 SSD
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

Figure 46. M.2 SSD Connector Location

Figure 47. M.2 SSD Installation

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1).
3. If installed, remove the riser card assembly to the left of the system (see Section 3.2.1).
4. Locate the M.2 Port 0 connector towards the back of the board. Refer to Figure 46 for the location.
5. Depending on the length of the M.2 SSD, use the onboard M.2 mounting stand-off at the appropriate location.
6. Align the notch within the SSD edge with the key in the server board M.2 connector and insert the SSD into the connector (see Letter A).

7. Using the fastener screw, secure the SSD to the M.2 mounting stand-off on the server board (see Letter B). Tighten to 1.5 in-lb.

8. Repeat steps 4–7 for M.2 Port 1, if needed.

9. Reinstall the riser card assembly, if needed (see Section 3.2.2).

10. Reinstall the system top cover (see Section 3.1.2).

### 3.6 Trusted Platform Module (TPM) Installation

This section provides instructions to install a Trusted Platform Module (TPM) in the system. Refer to the *Intel® Server M50CYP Family Configuration Guide* for available options.

#### Required Tools and Supplies

- Intel® TPM Accessory Kit
- Anti-static wrist strap and conductive workbench pad (recommended)

![Figure 48. Trusted Platform Module (TPM) Installation](image)

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1).
3. Locate the TPM module connector on the server board next to the PHM.
4. Insert the plastic stand-off into the server board mounting hole (see Letter A).
5. Place the TPM module over the connector and confirm the orientation of the module.
6. Press the module down onto the connector (see Better B).
7. Secure the TPM module to the stand-off with the fastener screw (see Letter C).
8. Reinstall the system top cover (see Section 3.1.2).
3.7 Intel® VROC 7.5 Key Installation

This section provides instructions to install an Intel® VROC Key in the system. Refer to the Intel® Server M50CYP Family Configuration Guide for available options.

Required Tools and Supplies

- Intel® VROC 7.5 Key
- Anti-static wrist strap and conductive workbench pad (recommended)

For more information on the Intel® VROC features, capabilities, and NVMe drive population rules, refer to the Intel® Server System M50CYP1UR Technical Product Specification.

Figure 49. Intel® VROC Key Insertion

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1).
3. Remove the Intel® VROC Key from its packaging.
4. Locate the white 4-pin key connector near the CR2032 battery.
5. To install the key, place it over the connector and confirm the orientation of the key matches that of the connector.
6. Press the key down onto the connector.
7. Reinstall the system top cover (see Section 3.1.2).
3.8 Intel® SAS Interposer Card Installation

This section provides instructions to install an Intel® SAS interposer card. Refer to the Intel® Server M50CYP Family Configuration Guide for available options.

Required Tools and Supplies

- Intel® SAS interposer card
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

Note: A SAS interposer card needs to be installed before installing a SAS RAID module.

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1).
3. Align the four screw holes of the SAS interposer card with the matching threaded holes on the base of the chassis (see Letter A).
4. Secure the SAS interposer card to the chassis using four screws (see Letter B). Tighten to 5 in-lb.
5. Attach all the connectors as required.
6. If the RAID module needs to be installed, see Section 3.9.
7. Reinstall the system top cover (see Section 3.1.2).
3.9 Intel® SAS RAID Module Installation

This section provides instructions to install an Intel® SAS Raid module. Refer to the Intel® Server M50CYP Family Configuration Guide for available options.

**Required Tools and Supplies**

- SAS RAID module
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

**Note:** A SAS interposer card needs to be installed before installing a SAS RAID module.

---

**Figure 51. Intel® SAS RAID Module Installation**

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1)
3. Remove the SAS RAID module from its packaging.
4. Insert the four plastic-barrel standoffs into the matching holes in the SAS interposer card (see Letter A).
5. Align the SAS RAID module mounting holes over the four standoffs (see Letter B).
6. Lower the SAS RAID module onto the SAS interposer card.
7. Press down firmly until the SAS RAID module connector is fully seated in the matching connector on the SAS interposer card (see Letter C). The module should be firmly seated over each standoff.
8. Insert a locking pin into each standoff (see Letter D).
9. Attach all the connectors as required.
10. Reinstall the system top cover (see Section 3.1.2).
3.10 Intel® RAID Maintenance Free Backup Unit (RMFBU) and Mounting Bracket Installation

This section provides instructions to install an RMFBU bracket and assembly in the system. Refer to the Intel® Server M50CYP Family Configuration Guide for available options.

Required Tools and Supplies

- RMFBU
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

The RMFBU assembly has the following elements:

- Latch
- Supercap capacitor
- RMFBU plastic case

The RMFBU bracket and assembly are installed in the same area inside the chassis as the SAS interposer card (see Section 3.8).

The three elements need to be assembled before attaching to the mounting bracket.

![Figure 52. Installing Latch on the Plastic Case](image)

1. Insert the latch in the opening on the plastic case (see Letter A).
2. Lift the latch holder slightly up (see Letter B) and insert the notch into opening until it clicks (see Letter C).
3. Bring the latch down until it is aligned vertically (see Letter D).

![Figure 53. Insert Super Cap](image)
4. Insert the super cap capacitor inside the plastic case with the cable protruding out. Choose the opening in the plastic case for the cable closest to the RAID module where the cable is going to be connected.

### 3.10.1 Intel® RMFBU Mounting Bracket Installation

![Figure 54. Intel® RMFBU Mounting Bracket Installation](image)

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 3.1.1).
3. Remove the cable clip, if installed, by unfastening the two screws holding the clip to the chassis.
4. Align the two screw holes of the RMFBU mounting bracket with the matching threaded holes on the base of the chassis (see Letter A).
5. Secure the RMFBU mounting bracket to the chassis using two screws (see Letter B). Tighten to 5 in-lb.
6. Slide the RMFBU assembly in the direction as indicated on the mounting bracket (see Letter C) until the latch locks in place.
7. Attach all the connectors as required.
8. Reinstall the system top cover (see Section 3.1.2).
4. System Software Updates and Configuration

The Intel® Server System M50CYP1UR includes a system software stack that consists of:

- System BIOS
- Baseboard Management Controller (BMC) firmware
- Intel® Management Engine (Intel® ME) firmware
- Field Replacement Unit (FRU)
- Sensor Data Record (SDR) data

Together, they configure and manage features and functions of the server system. A full software stack is installed during the system manufacturing process but may not be the latest available version. Intel highly recommends updating the full system software stack to the latest available version for optimal performance and system reliability. A System Update Package (SUP) containing the latest available system software stack can be downloaded from the following Intel website: http://downloadcenter.intel.com.

To ensure that the embedded platform management subsystem is configured properly, the latest FRU and SDR data must be installed after updating the full system software stack. Updated FRU and SDR data allows the platform management subsystem to monitor the specific system sensors used to determine appropriate system cooling, optimal performance, and accurate error reporting. FRU and SDR data is loaded by using the FRUSDR utility that is included with the System Update Package (SUP).

See the following Intel documents for more in-depth information about the system software stack and their functions:

- BIOS Firmware External Product Specification (EPS) – Intel NDA required

For guidelines and overview on BIOS Boot Menu, Setup, and hot keys, see the Intel® Server System M50CYP1UR Technical Product Specification.
5. **System Service – System Features Overview**

This chapter provides service personnel a reference to identify and locate the features associated with the Intel® Server System M50CYP1U options.

### 5.1 Front Drive Bay Options

![Figure 55. 4 x 2.5" Front Drive Bay Configuration](image)

![Figure 56. 12 x 2.5" Front Drive Bay Configuration](image)

### 5.2 Back Panel, Front Control Panel, Front I/O Features

![Figure 57. Back Panel Features](image)
Figure 58. Front Control Panel Features

Figure 59. Front I/O Features
5.3 Drive Bay LED Identification

Table 3. Drive Status LED States

<table>
<thead>
<tr>
<th>LED State</th>
<th>Drive Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>No access and no fault</td>
</tr>
<tr>
<td>Solid on</td>
<td>Hard drive fault has occurred</td>
</tr>
<tr>
<td>1 Hz blinking</td>
<td>RAID rebuild in progress</td>
</tr>
<tr>
<td>2 Hz blinking</td>
<td>Locate (identify)</td>
</tr>
</tbody>
</table>

Table 4. Drive Activity LED States

<table>
<thead>
<tr>
<th>Condition</th>
<th>Drive Type</th>
<th>LED Behavior</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power on with no drive activity</td>
<td>SAS/NVMe*</td>
<td>LED stays on</td>
</tr>
<tr>
<td>Power on with no drive activity</td>
<td>SATA</td>
<td>LED stays off</td>
</tr>
<tr>
<td>Power on with drive activity</td>
<td>SAS/NVMe</td>
<td>LED blinks off when processing a command</td>
</tr>
<tr>
<td>Power on with drive activity</td>
<td>SATA</td>
<td>LED blinks on when processing a command</td>
</tr>
<tr>
<td>Power on and drive spun down</td>
<td>SAS/NVMe</td>
<td>LED stays off</td>
</tr>
<tr>
<td>Power on and drive spinning up</td>
<td>SATA</td>
<td>LED stays off</td>
</tr>
<tr>
<td>Power on and drive spinning up</td>
<td>SAS/NVMe</td>
<td>LED blinks</td>
</tr>
<tr>
<td>Power on and drive spinning up</td>
<td>SATA</td>
<td>LED stays off</td>
</tr>
</tbody>
</table>

Note: The drive activity LED is driven by signals from the drive itself. Drive vendors may choose to operate the activity LED different from what is described in the above table. Should the activity LED on a given drive type behave differently than what is described, customers should reference the drive vendor specifications for the specific drive model to determine the expected drive activity LED operation.
5.4 Server Board Features

The following figure provides a general overview of the physical server board, identifying key feature and component locations.

![Intel® Server Board M50CYP2SB1U Component / Feature Identification](image-url)

**Figure 61. Intel® Server Board M50CYP2SB1U Component / Feature Identification**
The server board includes LEDs to identify system status and/or indicate a component fault. The following figures identify the diagnostic LEDs and their location on the server board.

![Image of server board with diagnostic LEDs labeled]

**Figure 62. Intel® light-Guided Diagnostics – LED Identification**

![Image of another view of the server board with diagnostic LEDs labeled]

**Figure 63. Exploded View of POST Code Diagnostic, System ID, and System Status LEDs Area**
The server board includes several jumper blocks that can be used to configure, protect, or recover specific features of the server board. The following figure identifies the location of each jumper block on the server board. For more information on the jumpers, see the Intel® Server Board M50CYP2SB Technical Product Specification (TPS).

Figure 65. Reset and Recovery Jumper Block Locations
6. System Service and FRU Replacement

This chapter provides instruction for replacement of system components considered to be field replaceable (FRU). Only system features that are identified as hot-swappable can be replaced while the system remains operational. These items include:

- Power Supply – In dual power supply configurations only
- Drives mounted within the front drive bay – Redundant RAID (1, 5, 6, and 10) configurations only
- System Fans

All other components within the system can only be serviced after the system has been powered off and AC power cords have been disconnected from the server system.

Before You Begin

Before integration of any system components, review all the safety and ESD precautions found in the Safety Warnings section at the beginning of this document.

System Reference

In the following procedures, all references to left, right, front, top, and bottom assume that the reader is facing the front of the server chassis.

Instruction Format

Each procedure described in this chapter follows an illustration first format. This format gives the reader the option to follow a quicker path to component integration by first seeing an illustration of the intended procedure. If necessary, the reader can then follow the step-by-step instructions that accompany each procedure.

6.1 System Cover Removal / Installation

The system top cover consists of two panels – one over the front half of the system and one over the back half of the system. To maintain system thermals, both top cover panels must always be in place when the system is operating.

Required Tools and Supplies

- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1
6.1.1 System Cover Removal

Removal of both top cover panels is necessary when installing or replacing any system component within the server chassis. Before removing the top cover, power down the system and unplug all peripheral devices and the power cable(s).

**Figure 66. System Top Cover Panel Shipping Screws**

The system ships from the factory with the front system cover panel and back system cover panel screwed to the chassis. A total of four screws, one on each side of the front panel and one on each side of the back panel, need to be removed to detach both top cover panels from the chassis.

**Note:** A non-skid surface or a stop behind the server system may be needed to prevent the server system from sliding on the work surface.

**Figure 67. System Cover Removal**

For each top cover panel:

1. While pushing down on both the left and right buttons of the given top panel (see Letter A), slide the top cover panel towards the front (front panel) or back (back panel) of the chassis (see Letter B).
2. Carefully lift the top cover panel up and away from the chassis.

**Note:** Each top cover panel can slide along the chassis base for 10 mm and then needs to be lifted.
### 6.1.2 System Cover Installation

![System Cover Installation](image)

For each top cover panel:

1. Carefully align and set the top cover panel on top of the chassis. Then, slide it inwards until it locks into place (see Letter A).

**Shipping Note:** When transporting the server system, Intel recommends installing the four top cover screws before shipping.

### 6.2 System Fan Replacement

Individual fans used in the Intel® Server System M50CYP1UR are hot-swappable.

**Required Tools and Supplies**
- Anti-static wrist strap and conductive workbench pad (recommended)

**Note:** Only individual fans can be removed or installed in the Intel® Server System M50CYP1UR. The entire system fan assembly cannot be removed from the chassis.

**Caution:** To minimize possible performance degradation and other thermal related issues, system fan replacement while the system is operational should be performed as quickly as possible.

System fans operate at very high speeds. Keep all tools and fingers away from all operational system fans when swapping out a defective fan.

Components within an operational system can get very hot. Avoid touching any components within the system while swapping out a defective system fan.
1. Remove the front top cover panel (see Section 6.1.1).
2. Locate the faulty fan. The LED on the faulty fan should be illuminated amber and rotor should NOT be turning.
3. Grasp the faulty system fan on both green marked ends and pull it up and away from the chassis (see Letter B). This step also disconnects the fan cable from the onboard 8-pin fan connector (see Letter A).
4. Carefully place the individual fan onto a flat surface.

5. Locate and unwrap the replacement fan.
6. Ensure that no cable(s) are within the replacement fan mounting zone within the fan assembly housing.
7. Align the fan connector with the matching server board connector and carefully lower the fan into the fan assembly housing. Gently push down until fully seated (see Letter A).
8. Ensure that the individual fan module connector is fully connected with baseboard fan connector (see Letter B).
9. Reinstall the system top cover (see Section 6.1.2).
6.3 Memory Module Replacement

Required Tools and Supplies
- Anti-static wrist strap and conductive workbench pad (recommended)
- Replacement equivalent memory module

Procedure Prerequisites
- Memory modules are NOT hot-swappable. The system must be powered down and unplugged from the AC power source before replacing a faulty memory module from the system.

The following figures show standard DDR5 DIMMs, but the steps of DDR5 DIMM replacement are the same for both standard DDR5 DIMMs and Intel Optane PMem modules.

DDR4 DIMM and Intel Optane PMem are commonly referred to as “memory module” in the following instructions.

Note: See Appendix C for general memory population rules.

Figure 71. Memory Module Removal

1. Ensure that the system power is off and disconnect the power cable(s).
2. Remove the top cover panels (see Section 6.1.1).
3. Identify and locate the DIMM to be removed.
4. Ensure that the ejection tabs of adjacent DIMM slots are closed.
5. Open the DIMM ejection tabs at both ends of the selected DIMM slot (see Letter A). The memory module will slightly lift from the slot.
6. Holding the memory module by its edges, lift it away from the slot (see Letter B).
Figure 72. Memory Module Installation

7. Ensure that the DIMM ejection tabs at both ends of the DIMM slot are pushed outward to the open position (see Letter A).
8. Carefully unpack the replacement memory module, taking care to only handle the device by its outer edges.
9. Align the notch at the bottom edge of the memory module with the key in the memory module slot (see Letter B).
10. Insert the memory module into the slot.
   a. Using even pressure along the top edge, push down on the memory module (see Letter C) until the ejection tabs of the memory slot snap into place (see Letter D).
11. Ensure that the ejection tabs are firmly in place (see Letter E).
12. Reinstall the system top cover (see Section 6.1.2).
6.4 Power Supply Replacement

**Caution:** The power supply is only hot-swappable (system does not have to be powered down) if the system is configured with two power supply modules operating in a 1+1 redundant configuration.

Systems with a single power supply installed or a system operating in a 2+0 non-redundant power mode MUST be powered OFF before removing the power supply module from the system.

![Power Supply Removal](CYP42620)

**Figure 73. Power Supply Removal**

1. Detach the power cord from the power supply to be removed.
2. Push and hold the green latch in the direction shown (see Letter A).
3. Use the handle to pull the power supply module from the system (see Letter B).

![Power Supply Installation](CYP42620)

**Figure 74. Power Supply Installation**

4. Locate the replacement power supply.
5. Slide the power supply into the power supply bay until it clicks and locks in place (see Letter A).
6.5 Processor Replacement

Components Required:
- New 3rd Gen Intel Xeon Scalable processor + included shipping tray
- Existing Processor carrier clip
- Existing 1U standard heat sink or 1U Enhanced Volume Air Cooling (EVAC) heat sink + new thermal interface material (TIM)

Note: Heat sinks are shipped pre-installed with TIM type Honeywell* PTM7000.

Required Tools and Supplies
- Anti-static wrist strap and conductive workbench pad (recommended)
- ESD Gloves (recommended)
- T-30 Torx* screwdriver
- Phillips head screwdriver #2

6.5.1 Processor Heat Sink Module (PHM) and Processor Removal

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 6.1.1).
3. Ensure all four heat sink anti-tilt wires are in the outward position (see Letter A).
4. Remove the two screws on the heat sink extension (see Letter B).
5. Then fully unscrew all four heat sink fasteners in any order (see Letter C).
6. Set all four anti-tilt wires on the heat sink to the inward position (see Letter D).
7. Lift the PHM straight up off the server board (see Letter E).
8. After removing the PHM, visually inspect that the socket is free of damage or contamination.

Caution: If debris is observed, blow it away gently. Do not use tweezers or any other hard tools to remove the debris.
9. If not replacing the processor, reinstall the socket cover.

![Figure 76. Reinstall the Socket Cover](image)

- Squeeze the finger grips at each end of the cover (see Letter A in above figure) and carefully lower the cover on the socket (see Letter B), then release finger grips.
- Ensure that socket cover is locked in place.

**Caution:** Do not press the center of the socket cover.

![Figure 77. Processor Removal from PHM Assembly](image)

10. Place the PHM, bottom side up, on a flat surface.
11. While holding down the PHM, rotate the lever (see Letter A) from left to right until the processor lifts from the processor carrier clip.
12. While holding the processor carrier clip, carefully lift the processor and slide it out of the processor carrier clip (see Letter B).
13. Return the lever to the original position (see Letter C).
14. Unlatch the tab on each corner of the processor carrier clip and lift the clip up to remove the processor carrier clip from the heat sink (see Letter D).

6.5.2 PHM and Processor Installation

To properly assemble the PHM and install it to the server board, the procedures described in the following sections must be followed in the order specified. These instructions assume that all the PHM components are new and the Thermal Interface Material (TIM) is already applied to the bottom of the heat sink.

6.5.2.1 Processor Heat Sink Module (PHM) Assembly

**Caution:** Wear ESD gloves to prevent electrostatic damage and oxidation or foreign material on processor package and land pads.

**Note:** The label on the heat sink refers to PHM installation onto the server board. It does not refer to the PHM assembly process.
1. Place the processor carrier clip on top of the processor while it is still on the tray.
2. Ensure that the Pin 1 indicator on the processor carrier clip is aligned with the Pin 1 indicator of the processor.

3. Gently press down on two opposite sides at a time of the processor carrier clip until it clicks into place.
4. Remove the heat sink from its packaging. To avoid damage to the heat sink, grasp it by its narrower top and bottom edges.

5. Set the anti-tilt wires to the outward position.
6. Turn the heat sink over and place it bottom side up on a flat surface.
7. Remove the plastic protective film from the Thermal Interface Material (TIM).
8. Align Pin 1 indicator of processor carrier clip with the corner cut-out on the heat sink. For the EVAC heat sink, align the processor carrier clip and the heat sink as shown in the above figure.

**Note:** For the standard heat sink, there are two cut-out corners; either can be used to align Pin 1 indicators.

9. Gently press down the heat sink onto the processor carrier clip until it clicks into place.
10. Ensure that all four heat sink corners are securely latched to the carrier clip tabs.

### 6.5.2.2 Processor Installation

**Caution:** Do not touch the socket pins. The pins inside the processor socket are extremely sensitive. A damaged processor socket may produce unpredictable system errors.

1. Remove the protective cover by squeezing the finger grips (see Letter A) and pulling the cover up (see Letter B).
2. Ensure that the socket is free of damage or contamination before installing the PHM.

**Caution:** If debris is observed, blow it away gently. Do not remove it manually, such as with tweezers.
3. Align Pin 1 indicators of the processor carrier clip and processor with Pin 1 indicator on the bolster plate.

**Caution:** Processor socket pins are delicate and bend easily. Use extreme care when placing the PHM onto the processor socket. Do not drop it.

4. Set all four anti-tilt wires on the heat sink to the inward position (see Letter A).
5. Holding the PHM horizontally, carefully lower it on to the bolster plate’s alignment pins (see Letter B).
6. Set all four anti-tilt wires on the heat sink to the outward position (see Letter D).
7. Using a Phillips #2 screwdriver, tighten the heat sink extension screws (see Letter C).
8. Tighten the heat sink fasteners using a T30 Torx screwdriver to 8 in-lb. No specific sequence is needed for tightening.
9. Reinstall the system top cover (see Section 6.1.2).

**Important:** Do not install a processor heat sink on an empty socket. Also, only install a socket cover on an empty socket.

### 6.6 Riser Card Replacement

This section provides instructions for the replacement of a riser card.

**Required Tools and Supplies**
- Replacement riser card option
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1
There are multiple types of riser card brackets. The replacement instructions are the same for each type.

**Figure 88. Riser Card Bracket Removal**

1. Power off the system and disconnect the system power cord(s).
2. Remove the system top cover (see Section 3.1.1).
3. If present, disconnect all cables (internal or external) that may be attached to the riser assembly.
4. Loosen the two screws on the side of the riser card bracket (see Letter A).
5. Grasp the riser card bracket with both hands and carefully pull it up and away from the chassis. (see Letter B).

6. If an add-in card is present, remove it from the riser card bracket assembly.

**Figure 89. Add-in Card Removal from Bracket**

- Remove the screw that secures the add-in card to the riser card bracket (see Letter A)
- Carefully remove the add-in card to the riser card bracket (see Letter B)
7. Remove the riser card from the Bracket.

![Figure 90. Riser Card Removal from Bracket](image)

- Remove the two screws that secure the riser card to the bracket (see Letter A).
- Carefully Remove the riser card from the bracket (see Letter B).

8. Locate and unpack the replacement riser card.

9. Install the riser card to the riser card bracket.

**Note:** The following installation instructions for riser card to bracket are the same for all included riser card brackets and supported riser card options.

![Figure 91. Riser Card Installation onto the Bracket](image)

- Align and mount the riser card to the bracket using the mounting holes.
- Using the fastener screws, secure the riser card to the bracket. Tighten to 5 in-lb.

To install the add-in card in the riser card, the add-in card connected to the PCIe interposer riser card or the riser card in Riser Slot #1 must be oriented with component side up as shown in Figure 92. The add-in card connected to the riser card in Riser Slot #2 must be oriented with component side down as shown in Figure 93.

![Figure 92. PCIe* Add-in Card Installation for Riser Card on Riser Slot #1 or PCIe* Interposer Riser Card](image)
Note: Riser Slot #3 is only used to provide an additional PCIe NVMe interface to the hot-swap backplane mounted to the front drive bay.

10. Insert the add-in card until it is fully seated inside the PCIe slot on the riser card (see Letter A).
11. Using the fastener screws, secure the add-in card to the riser card assembly (see Letter B). Tighten to 5 in-lb.

Note: For add-in cards with internal cable connectors, it may be necessary to connect cable(s) before installing the riser card assembly into the system. See to Appendix B for cable routing guidance.

The following riser card assembly installation steps are the same for Riser Slot #1, Riser Slot #2, and PCIe Interposer Slot.

12. Position the add-in card assembly's edge connector over the riser slot on the server board.
13. Align the two key slots on the back edge of the add-in card assembly with the mounting keys on the back of the chassis.
14. Once aligned, press the add-in card assembly straight down into the riser slot (see Letter A).
15. Using the fastener screws, secure the add-in card assembly to the system (see Letter B). Tighten to 5 in-lb.
16. Connect any cables to the add-in card that are required. See your add-in card documentation for additional information.
17. Reinstall the system top cover (see Section 6.1.2).
6.7 Front Drive Replacement

The Intel® Server System M50CYP1UR has front drive bay chassis options that support 2.5" form factor drives (SSDs only). Additionally, 7 mm thick SSDs with a form factor of 2.5" are supported when used with the supplied drive blank for 2.5" bays. Each storage drive that interfaces with a backplane is mounted to a tool-less, non-detachable, hot swap drive mounting rail.

This section provides instructions for front drive replacement. Figure 95 identifies the drive bay components. For drive population rules, see the Intel® Server System M50CYP1UR Technical Product Specification (TPS).

**Required Tools and Supplies**

- Replacement 2.5” Solid State Drives (SSDs)
- Anti-static wrist strap and conductive workbench pad (recommended)

The following figure identifies the 2.5” drive bay components.

![Drive Bay Components](Figure 95. Drive Bay Components)

**Note:** To ensure proper system airflow requirements, all front drive bays must be populated with either a drive or supplied drive blank.

**Note:** The 2.5” drive mounting rails in the system are not removable. They slide out so that the storage drives can be installed or removed. When sliding out a drive mounting rail from the system, only pull it as much as it allows without forcing it.

6.7.1 2.5" SSD Drive Removal

![2.5" SSD Drive Removal](Figure 96. 2.5" 7 mm Drive Removal)
Figure 97. 2.5” 15 mm Drive Removal

1. Press the button on the drive extraction lever to release it (see Letter A).
2. Using the lever, pull the drive mounting rail part way out of the drive bay (see Letter B).

**Note:** The 2.5” drive mounting rail cannot be pulled all the way out of the chassis. Only pull it as much as it allows without forcing it.

3. Remove the drive from the drive mounting rail (see Letter C).

### 6.7.2 2.5” SSD Drive Assembly for 7 mm Drives

The server system supports 2.5” SSDs with 7 mm of thickness when used with the supplied blanks. The supplied blank for 2.5” bays has two parts: top and bottom. The top part must be attached to the 7 mm drive to fit properly inside the 2.5” bay. This section provides instructions to attach the supplied blank part to a 7 mm thick SSD. The 2.5” SSDs with 15-mm thick drive do not require any assembly with the top part of the drive blank.

Figure 98. Separating Top and Bottom Parts of Drive Blank

1. Press the handles at the bottom part of the drive blank (see Letter A).
2. Separate the top and bottom parts while pressing the handles (see Letter B).
3. Insert the 2.5-inch 7 mm SSD drive into the top part of the drive blank as shown in the above figure.

**Note:** Inserting it in the opposite direction could potentially scratch the gold fingers of the SSD connector.

### 6.7.3 2.5” SSD Drive Installation
1. Ensure that the drive extraction lever is in the open position and the drive mounting rail is pulled out halfway.
2. Align the drive assembly with the open drive bay.
3. Insert the assembled 7-mm thick drive or non-assembled 15-mm thick drive into the drive bay (see Letter A).

**Note:** Intel recommends holding the drive with one hand while holding the lever with the other hand.

4. Slide the drive forward until it contacts the backplane (see Letter B).
5. Complete the drive installation by closing the drive extraction lever until it locks into place (see Letter C).
6.8  2.5” Drive Mounting Rail Replacement

Required Tools and Supplies

- Drive mounting rail kit
- 2.5” drive mounting rail kit
- Anti-static wrist strap and conductive workbench pad (recommended)

The following figure shows the backside of the drive mounting rail identifying the two mounting rail studs and the tab.

![Figure 104. Drive Mounting Rail Identification](image)

1. Remove the drive mounting rail from the front drive bay.

![Figure 105. Removing Drive Mounting Rail](image)

- Lift the tab on the drive mounting rail slightly up using a flat head screwdriver to detach the mounting rail from the chassis (see Letter A).
- Carefully lift the drive mounting rail out and away from the chassis.

**Note:** Handle the drive mounting rail with care. Do not bend or twist it.
2. Install the drive mounting rail in the front drive bay.

![Image of drive mounting rail installation](image)

**Figure 106. Installing Drive Mounting Rail**

- Align the two drive mounting rail mounting studs with the slot on the side of the bay (see Letter A).
- With the mounting studs in the slot, slide the mounting rail towards the back of the bay until the drive mounting rail tab locks in place (see Letter B).

**Note:** Handle the drive mounting rail with care. Do not bend or twist it.

### 6.9 Ethernet Network Adapter for OCP* Replacement

This section provides instructions for OCP adapter replacement from the chassis.

**Required Tools and Supplies**

- OCP Ethernet Network Adapter
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

The OCP adapter bay has a filler that needs to be removed before replacing any of the OCP adapter configurations.

**Caution:** If the OCP adapter is removed and the bay becomes empty again, this filler needs to be reinstalled.

The following steps show how to remove and install the OCP adapter bay filler.

![Image of OCP adapter bay filler removal](image)

**Figure 107. OCP* Adapter Bay Filler Removal**
To remove the filler:
1. Loosen the thumb screw on the right side of the filler (see Letter A).
2. Pull out the right side of the filler first and move the filler slightly to the right, ensuring the left end of the filler is out of the slot.
3. Pull the filler away from the chassis (see Letter B).

![Figure 108. OCP* Adapter Bay Filler Installation](image)

To install the filler:
4. Insert the left side of the filler on the slot.
5. Push the filler until the mounting screw goes into the mounting hole on the right side (see Letter A).
6. Tighten the thumb screw (see Letter B).

### 6.9.1 OCP* Adapter with Pull Tab Replacement

1. Loosen the thumb screw on the right side of the OCP adapter (see Letter A).
2. Pull the OCP adapter out of the bay using the tab on the left side of the OCP adapter (see Letter B).

![Figure 109. OCP* Adapter with Pull Tab Removal](image)
3. Align the OCP adapter with the open OCP bay slot and slide forward until the connectors make secure contact (see Letter A).
4. Tighten the thumb screw on the right side of the OCP adapter (see Letter B).

6.9.2 OCP* Adapter with Ejector Latch Replacement

1. Push the latch on the left side of the OCP adapter (see Letter A) and pull the latch out (see Letter B).
2. Using the latch, pull the adapter out of the system (see Letter C).
Figure 112. OCP* Adapter with Ejector Latch Installation

3. Ensure that the latch is in the open position.
4. Push the OCP adapter forward until the right side of the latch contacts the rear panel (see Letter A).
5. Rotate the latch towards the inside to get the OCP adapter engaged with the connectors and close the latch (see Letter B).

6.9.3 OCP* Adapter with Internal Lock Replacement

All L6 and L9 integrated systems come with an internal lock on the OCP rail. This lock is a piece of blue plastic. The OCP rail in the system has a dedicated space to accommodate the lock. The lock can be mounted on the rail in two different orientations. When the keying features of the lock are facing up, it is in an unlocked orientation. When the keying features are facing down, it is in a locked orientation. A lock symbol is included in each side of the plastic lock to indicate its orientation. The following figure shows the features of the lock.

Figure 113. Internal Lock with Unlock and Lock Orientation
1. Remove the system top cover (see Section 6.1.1).
2. Remove any riser card assembly above the OCP adapter area if present (see Section 6.6).
3. Squeeze the two hooks of the internal lock and pull it out (see Letter A).
4. Install it back in the chassis in reverse orientation (see Letter B).
5. Push the OCP adapter out of the bay from inside the chassis (see Letter C).

6. Squeeze the two hooks of the internal lock and pull it out (see Letter A).
7. Align the OCP adapter with the open OCP bay slot and slide forward until the connectors make secure contact (see Letter B).
8. Reinstall the internal lock with the lock orientation (see Letter C).
9. Reinstall the riser card assembly as needed (see Section 6.6).
10. Reinstall the system top cover (see Section 6.1.2).
6.10 M.2 Storage Device Replacement

The server board includes two M.2 connectors as shown in the following figure. Each M.2 connector supports a PCIe NVMe or SATA SSD drive that conforms to a 22110 (110 mm) or 2280 (80 mm) form factor.

**Required Tools and Supplies**

- Replacement M.2 SSD
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

![Figure 116. M.2 SSD Removal](image)

1. Power off the system and disconnect the system power cord(s).
2. Remove the system top cover (see Section 6.1.1).
3. If installed, remove the riser card assembly to the left of the system (see Section 6.6).
4. Locate the M.2 to be replaced towards the back of the server board.
5. Remove the screw to free up one end of the M.2 SSD (see Letter A).
6. Carefully lift the free end of the M.2 SSD and gently remove it from the connector in the direction shown (see Letter B).

![Figure 117. M.2 SSD Installation](image)

7. Locate and carefully unpack the replacement M.2 SSD. Hold it by its edges.
8. Depending on the length of the M.2 SSD, use the onboard M.2 mounting stand-off at the appropriate location.
9. Align the notch in the SSD edge with the key in the server board M.2 connector and insert the SSD into the connector (see Letter A).
10. Using the fastener screw, secure the SSD to the M.2 mounting stand-off on the server board (see Letter B). Tighten to 1.5 in-lb.
11. Reinstall the riser card assembly if needed (see Section 6.6).
12. Reinstall the system top cover (see Section 6.1.2).
6.11 Trusted Platform Module (TPM) Replacement

This section provides instructions to replace a Trusted Platform Module (TPM) in the system. Refer to the Intel® Server M50CYP Family Configuration Guide for available options.

Required Tools and Supplies

- Intel® TPM Accessory Kit
- Anti-static wrist strap and conductive workbench pad (recommended)

![Figure 118. Trusted Platform Module (TPM) Removal](image)

1. Power off the system and disconnect the system power cord(s).
2. Remove the system top cover (see Section 6.1.1).
3. Locate the TPM module connector on the server board next to the PHM.
4. Remove the fastener screw on the TPM module (see Letter A).
5. Gently remove and lift the TPM module away from the connector (see Letter B).
6. (If needed) remove the plastic stand-off from the server board mounting hole.

![Figure 119. Trusted Platform Module (TPM) Installation](image)

7. Insert the plastic stand-off into the server board mounting hole (see Letter A).
8. Place the TPM module over the connector and confirm the orientation of the module.
9. Press the module down onto the connector (see Letter B).
10. Secure the TPM module to the stand-off with the fastener screw (see Letter C).
11. Reinstall the system top cover (see Section 6.1.2).
6.12 Intel® VROC 8.0 Key Replacement

This section provides instructions to replace an Intel® VROC Key in the system. See the Intel® Server M50CYP Family Configuration Guide for available options.

**Required Tools and Supplies**
- Intel® VROC 8.0 Key
- Anti-static wrist strap and conductive workbench pad (recommended)

For more information on the Intel® VROC features, capabilities, and NVMe drive population rules, see the Intel® Server System M50CYP1UR Technical Product Specification.

**Figure 120. Intel® VROC Key Removal**

1. Power off the system and disconnect the system power cord(s).
2. Remove the system top cover (see Section 6.1.1).
3. Using the key pull tab, pull the key up until it disengages from the connector.

**Figure 121. Intel® VROC Key Insertion**

4. Remove the Intel® VROC Key from its packaging.
5. Locate the white 4-pin key connector near the CR2032 battery.
6. To install the key, place it over the connector and confirm the orientation of the key matches that of the connector.
7. Press the key down onto the connector.
8. Reinstall the system top cover (see Section 6.1.2).
6.13 Intel® SAS Interposer Card Replacement

This section provides instructions to replace an Intel® SAS interposer card. Refer to the Intel® Server M50CYP Family Configuration Guide for available options.

Required Tools and Supplies

- Replacement Intel® SAS Interposer Card
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

Note: A SAS interposer card needs to be installed before installing a SAS RAID module.

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 6.1.1).
3. If an installed RAID module needs to be removed, see Section 6.14.
4. Disconnect all the connectors attached to the SAS interposer card.
5. Unscrew the four screws on the SAS interposer card (see Letter A).
6. Lift the SAS interposer card from the chassis all the way out (see Letter B).

Figure 122. Intel® SAS Interposer Card Removal
7. Remove the replacement SAS interposer card from its packaging.
8. Align the four screw holes of the SAS interposer card with the matching threaded holes on the base of the chassis (see Letter A).
9. Secure the SAS interposer card to the chassis using four screws (see Letter B). Tighten to 5 in-lb.
10. Attach all the connectors as required.
11. If the RAID module needs to be installed, see Section 6.14.
12. Reinstall the system top cover (see Section 6.1.2).
6.14 Intel® SAS RAID Module Replacement

This section provides instructions to replace an Intel® SAS Raid module. Refer to the Intel® Server M50CYP Family Configuration Guide for available options.

Required Tools and Supplies

- Replacement SAS RAID module
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

Note: A SAS interposer card needs to be installed before installing a SAS RAID module.

Figure 124. Intel® SAS RAID Module Removal

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 6.1.1).
3. Disconnect all the cables connected to the SAS RAID module.
4. Remove the four locking pins from each standoff (see Letter A).
5. Grasp the SAS RAID module on both sides and pull up firmly until the connector is disengaged from the matching connector on the SAS interposer card (see Letter B).
6. Remove the standoffs (see Letter C) if not installing another SAS RAID module on the SAS interposer card. Keep the standoffs, locking pins, and the SAS RAID module together.
The Intel® SAS RAID Module installation requires that the SAS interposer card is already installed. See the SAS interposer card installation steps in Section 6.13.

7. Remove the SAS RAID module from its packaging.
8. Insert the four plastic-barrel standoffs into the matching holes in the SAS interposer card (see Letter A).
9. Align the SAS RAID module mounting holes over the four standoffs (see Letter B).
10. Lower the SAS RAID module onto the SAS interposer card.
11. Press down firmly until the SAS RAID module connector is fully seated in the matching connector on the SAS interposer card (see Letter C). The module should be firmly seated over each standoff.
12. Insert a locking pin into each standoff (see Letter D).
13. Attach all the connectors as required.
14. Reinstall the system top cover (see Section 6.1.2).
6.15 Intel® RAID Maintenance Free Backup Unit (RMFBU) and Mounting Bracket Replacement

This section provides instructions to replace an RMFBU bracket and assembly in the system. See the Intel® Server M50CYP Family Configuration Guide for available options.

Required Tools and Supplies

- Replacement RMFBU
- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

![Figure 126. Intel® RMFBU Mounting Bracket Removal - Chassis](image)

1. Remove the system top cover (see Section 6.1.1).  
2. Disconnect all the connectors attached to the RMFBU.  
3. Lift the latch up slightly to unlock the RMFBU assembly from the mounting bracket (see Letter A).  
4. Slide the RMFBU assembly (see Letter B) in the direction as indicated on the top of the RMFBU mounting bracket.  
5. Unscrew the two screws on the RMFBU mounting bracket (see Letter C).  
6. Pull the RMFBU mounting bracket from the chassis all the way out (see Letter D).

The RMFBU assembly has the following elements:

- Latch  
- Supercap capacitor  
- RMFBU plastic case

The RMFBU bracket and assembly are installed in between the system fans and hot-swap backplane. The three elements need to be assembled before attaching to the mounting bracket.
7. Insert the latch in the opening on the plastic case (see Letter A).
8. Lift the latch holder slightly up (see Letter B) and insert the notch into opening until it clicks (see Letter C).
9. Bring the latch down until it is aligned vertically (see Letter D).

10. Insert the super cap capacitor inside the plastic case with the cable protruding out. Choose the opening in the plastic case for the cable closest to the RAID module where the cable is going to be connected.

11. Remove the cable clip, if installed, by unfastening the two screws holding the clip to the chassis.
12. Align the two screw holes of the RMFBU mounting bracket with the matching threaded holes on the base of the chassis (see Letter A).
13. Secure the RMFBU mounting bracket to the chassis using two screws (see Letter B). Tighten to 5 in-lb.
14. Slide the RMFBU assembly in the direction as indicated on the mounting bracket (see Letter C) until the latch locks in place.
15. Attach all the connectors as required.
16. Reinstall the system top cover (see Section 6.1.2).
6.16 Backplane Replacement

The Intel® Server System M50CYP1UR comes with either a 4 x 2.5" backplane or 12 x 2.5" backplane. The 4 x 2.5" backplane supports up to four SAS/SATA/PCIe NVMe drives. The 12 x 2.5" backplane supports up to twelve SAS/SATA/PCIe NVMe drives. For more information on front drive bay support, refer to the Intel® Server System M50CYP1UR Technical Product Specification (TPS).

6.16.1 4 x 2.5" Backplane Replacement

Required Tools and Supplies

- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

1. Power off the system and disconnect the power cable(s).
2. Remove the system top cover (see Section 6.1.1).
3. Remove all drives and drive blanks from the front drive bays.
4. Disconnect all cables from the backplane.
5. Remove all fastener screws securing the backplane to drive bay (see Letter A).
6. Slide the backplane up from the bottom of the chassis and remove the backplane from the server chassis (see Letter B).

![Figure 130. 4 x 2.5" Backplane Removal](CYP41040)
7. Locate the replacement backplane.

**Note:** Hold the backplane only by the edges. Do not push or pull on any components on the backplane.

8. Position the backplane to align with the pink highlighted areas shown in the above figure (see Letter A).
9. Slide the backplane down to lock it into place.
10. Secure the backplane with the fastener screws as shown (see Letter B).
11. Reinstall the drives and/or drive blanks as needed.
12. Reinstall the system top cover (see Section 6.1.2).
6.16.2 12 x 2.5" Backplane Replacement

Required Tools and Supplies

- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #1

Figure 132. 12 x 2.5" Backplane Removal

1. Power off the system and disconnect the power cable(s)
2. Remove the system top cover (see Section 6.1.1).
3. Remove all drives and drive blanks from the front drive bays.
4. Disconnect all cables from the backplane.
5. Remove all fastener screws securing the backplane to drive bay (see Letter A).
6. Slide the backplane up from the bottom of the chassis and remove the backplane from the server chassis (see Letter B).
7. Locate the replacement backplane

**Note:** Hold the backplane only by the edges. Do not push or pull on any components on the backplane.

8. Position the backplane to align with the pink highlighted areas shown in the above figure (see Letter A).
9. Slide the backplane down to lock it into place
10. Secure the backplane with the fastener screws as shown (see Letter B).
11. Reinstall the drives and/or drive blanks as needed.
12. Reinstall the system top cover (see Section 6.1.2).

### 6.16.3 4 x 2.5" Backplane and 12 x 2.5" Backplane Cabling

Each installed NVMe drive must have PCIe signals cabled to the appropriate backplane SlimSAS connector from any of the following PCIe signal sources:

- Available onboard x4 PCIe SlimSAS connectors on the server board
- Optional tri-mode RAID add-in card
- NVMe riser card with SlimSAS connectors

Each installed SAS/SATA drive must have SAS/SATA signals cabled to the appropriate backplane SAS/SATA connector from any of the following SAS/SATA signal sources:

- Available onboard two 4-port Mini-SAS HD connectors on the server board
- Optional tri-mode RAID add-in card
- SAS/SATA Raid module

Available riser cards, PCIe SlimSAS cables, and SAS interposer card along with the accessory kits are sold separately from the system. See the *Intel® Server M50CYP Family Configuration Guide* to determine the appropriate cables necessary to match the desired NVMe and SAS/SATA drive configuration to a specified PCIe source. Cables identified in the configuration guide are optimized to provide the cleanest cable routing.

All cables should be routed using the cable channels as shown in the following illustration. See the cable routing procedure below.
The following procedure should be followed when installing data cables from a backplane to a PCIe or SAS/SATA source:

1. Locate the PCIe SlimSAS or SAS/SATA cable that supports the desired NVMe or SAS/SATA drive configuration and PCIe source.
2. Attach one end of the cable to matching PCIe* _SSD SlimSAS or SAS/SATA connectors on the backplane.
3. Cables routed from the backplane to the server board SlimSAS connectors are routed through the clearance in the middle of the fan assembly.
4. Cables routed to connectors on add-in cards and/or riser cards and/or server board Mini-SAS HD connectors are routed through the right chassis side wall. Remove the cable side wall by unfastening the two screws holding the side wall to the chassis.
5. Connect the other end of the PCIe SlimSAS or SAS/SATA cable to the appropriate PCIe source (server board or optional sources).
6. Using the two sidewall screws, attach the sidewall to the chassis.
6.17 System Battery Replacement

Required Tools and Supplies

- Replacement battery
- Anti-static wrist strap and conductive workbench pad (recommended)

---

**Figure 135. Replacing the System Battery**

1. Remove the system top cover (see Section 6.1.1).
2. If present, remove riser card assemblies above the battery location (see Section 3.2.1).
3. Locate the battery on the server board. (see above figure).
4. Gently press the metal clip as shown to release the battery (see Letter A).
5. Remove the battery from the plastic socket (see Letter B).
6. Dispose of the battery according to local laws.
7. Remove the new lithium battery from its package and, being careful to observe the correct polarity, insert it into the battery socket.
8. Reinstall riser card assemblies if needed (see Section 3.2.2).
9. Reinstall the system top cover (see Section 6.1.2).
10. Use the <F2> BIOS Setup Utility to restore BIOS Settings and reset the system time and date.
6.18 Server Board Replacement

Required Tools and Supplies

- Anti-static wrist strap and conductive workbench pad (recommended)
- Phillips head screwdriver #2

![Figure 136. Server Board Removal](image)

1. Power off system and remove power cords from each power supply module installed.
2. Disconnect all externally attached cables.
3. Remove the system top cover (see Section 6.1.1).
4. Remove power supply modules (see Section 6.4).
5. Remove individual system fan (see Section 6.2).
6. Disconnect all cables attached to add-in cards and I/O modules.
7. Remove riser card assemblies (see Section 3.2.1).
8. Remove all options installed onto the server board including (if installed): TPM Module, M.2 SSD, Intel® VROC 7.5 key, OCP adapter, Intel® SAS interposer card and SAS RAID module.
9. Remove processors (see Section 6.5).
10. Remove all DIMMs (see Section 6.3).
11. Disconnect and clear from the server board area all cables attached to connectors on the server board.
12. Remove 17 screws used to secure the server board to the chassis (see Letter 'A').
13. Slide the server board slightly towards the front of the chassis to disengage from the chassis cut-outs the connectors on the rear edge of the board.
14. Carefully lift the server board from the chassis and place it into an anti-static bag.
15. Verify that all cables are clear of the area in which the server board will be installed.
16. Remove the server board from its anti-static bag.
17. Carefully lower the server board into the chassis. Then, slide the server board slightly towards the back of the chassis until the VGA port goes through the cut-out on the rear panel and the screw holes align with the chassis standoffs (see Letter A).
18. Fasten the server board to the chassis using all the screws (see Letter B), including the DIMM guard brackets and the riser 2 guiding bracket (colored green in the above figure). Tighten to 5 in-lb.
19. Re-attach all cables previously removed from the server board.
20. Install processors (see Section 6.5).
21. Install DIMMs (see Section 6.3).
22. Reinstall all options previously removed from the server board.
23. Reinstall riser card assemblies (see Section 3.2.4).
24. Re-attach all internal cables previously detached from add-in cards and I/O modules.
25. Reinstall individual system fan (see Section 6.2).
26. Install power supply modules (see Section 6.4).
27. Reinstall the system top cover (see Section 6.1.2).
Available Intel support options with your Intel Server System:


Information available at the support site includes:
- Latest BIOS, firmware, drivers, and utilities
- Product documentation, setup, and service guides
- Full product specifications, technical advisories, and errata
- Compatibility documentation for memory, hardware add-in cards, and operating systems
- Server and chassis accessory parts list for ordering upgrades or spare parts
- A searchable knowledge base to search for product information throughout the support site

Quick Links:

<table>
<thead>
<tr>
<th>Use the following links for support on Intel Server Boards and Server Systems</th>
<th>Download Center</th>
<th>BIOS Support Page</th>
<th>Troubleshooting Boot Issue</th>
</tr>
</thead>
</table>

Use the following links for support on Intel® Data Center Block (DCB) Integrated Systems*  
* Intel DCB comes pre-populated with processors, memory, storage, and peripherals based on how it was ordered through the Intel Configure to Order tool.

<table>
<thead>
<tr>
<th>Use the following links for support on Intel® Data Center Block (DCB) Integrated Systems*</th>
<th>Download Center</th>
<th>Technical Support Documents</th>
<th>Warranty and Support Info</th>
</tr>
</thead>
</table>

2. If a solution cannot be found at Intel's support site, submit a service request via Intel's online service center at https://supporttickets.intel.com/servicecenter?lang=en-US. In addition, you can also view previous support requests. (Login required to access previous support requests)

3. Contact an Intel support representative using one of the support phone numbers available at https://www.intel.com/content/www/us/en/support/contact-support.html (charges may apply).

Intel also offers Partner Alliance Program members around-the-clock 24x7 technical phone support on Intel® server boards, server chassis, server RAID controller cards, and Intel® Server Management at https://www.intel.com/content/www/us/en/partner-alliance/overview.html

**Note:** The 24x7 support number is available after logging in to the Intel Partner Alliance website.

**Warranty Information**

Appendix B. Internal Cable Routing Channels

The system provides cable routing channels along each chassis sidewall. Cables should not be routed directly in front of the system fans or through the center of the server board between the memory slots and processor sockets.

**Note:** The system fan assembly must be removed before routing cables.

![System Cable Routing Channels](image)
Appendix C. General Memory Population Rules

In the Intel® Server System M50CYP1UR, a total of 32 DIMM slots are provided – two processors, eight memory channels per processor, and two DIMMs per channel. The following figure identifies DIMM slots on the server board.

**Note:** All black DIMM slots must be populated with either DIMMs or supplied DIMM blanks. All system configurations ship from Intel with DIMM blanks pre-installed. Pre-installed DIMM blanks should only be removed when installing a memory module in its place.

![Memory Slot Layout](image)

**Figure 139. Memory Slot Layout**

C.1 DDR4 DIMM Population Rules

**Note:** Although mixed DDR4 DRAM DIMM configurations are supported, Intel only performs platform validation on systems that are configured with identical DIMMs installed.

The following memory population rules apply when installing DDR4 DIMMs:

- **Mixing rules:**
  - Mixing DDR4 DIMMs of different frequencies and latencies is not supported within or across processors. If a mixed configuration is encountered, the BIOS attempts to operate at the highest common frequency and the lowest latency possible.
  - x4 and x8 width DDR4 DIMMs may be mixed in the same channel.
  - Mixing of DDR4 DIMM types (RDIMM, LRDIMM, 3DS-RDIMM, 3DS-LRDIMM) within or across processors is not supported. This situation is a Fatal Error Halt in Memory Initialization.
- For a single DDR4 DIMM in a dual-slot channel, populate slot 1 (blue slot).
- For multiple DDR4 DIMMs per channel:
  - When populating a quad-rank DIMM with a single- or dual-rank DDR4 DIMM in the same channel, the quad-rank DDR4 DIMM must be populated farthest from the processor. Incorrect DDR4 DIMM placement results in an MRC error code. A maximum of 8 logical ranks can be used on any one channel, as well as a maximum of 10 physical ranks loaded on a channel.
  - For RDIMM, LRDIMM, 3DS-RDIMM, and 3DS-LRDIMM, always populate DDR4 DIMMs with higher electrical loading in slot 1 (blue slot) followed by slot 2 (black slot).
- Memory slots associated with a given processor are unavailable if the corresponding processor socket is not populated.
- Processor sockets are self-contained and autonomous. However, all memory subsystem support (such as memory RAS and error management) in the BIOS Setup are applied commonly for each installed processor.
- For best system performance, memory must be installed in all eight channels for each installed processor.
- For best system performance in dual processor configurations, installed DDR4 DIMM type and population for DDR4 DIMMs configured to CPU 1 must match DDR4 DIMM type and population configured to CPU 0.

### C.2 Intel® Optane™ Persistent Memory 200 Series Module Rules

All operating modes:
- Only Intel Optane persistent memory 200 series modules are supported.
- Intel Optane persistent memory 200 series modules of different capacities cannot be mixed within or across processor sockets.
- Memory slots supported by the Integrated Memory Controller 0 (IMC 0) (memory channels A and B) of a given processor must be populated before memory slots on other IMCs.
- For multiple DIMMs per channel:
  - Only one Intel Optane persistent memory 200 series module is supported per memory channel.
  - Intel Optane persistent memory 200 series modules are supported in either DIMM slot when mixed with LRDIMM or 3DS-LRDIMM.
  - Intel Optane persistent memory 200 series modules are only supported in DIMM slot 2 (black slot) when mixed with RDIMM or 3DS-RDIMM.
- No support for SDRAM SRx8 DIMM that is populated within the same channel as the Intel Optane persistent memory 200 series module in any operating mode.
- Ensure that the same DDR4 DIMM type and capacity is used for each DDR4 + Intel Optane persistent memory 200 series module combination.

Memory mode:
- Populate each memory channel with at least one DDR4 DIMM to maximize bandwidth.
- Intel Optane persistent memory 200 series modules must be populated symmetrically for each installed processor (corresponding slots populated on either side of each processor).

App Direct mode:
- Minimum of one DDR4 DIMM per IMC (IMC 0, IMC 1, IMC 2 and IMC 3) for each installed processor.
- Minimum of one Intel Optane persistent memory 200 series module for the board.
- Intel Optane persistent memory 200 series modules must be populated symmetrically for each installed processor (corresponding slots populated on either side of each processor).
### Table 5. Intel® Optane™ Persistent Memory 200 Series Module Support

<table>
<thead>
<tr>
<th>Processor Shelf</th>
<th>Intel® Optane™ Persistent Memory 200 Series Capacity (GB)</th>
<th>Speed (MT/s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Silver 4300 processors (Silver 4314 processor SKU only)</td>
<td>128 256 512</td>
<td>2666 2400</td>
</tr>
<tr>
<td>Gold 5300 processors</td>
<td>128 256 512</td>
<td>2933 2666 2400</td>
</tr>
<tr>
<td>Gold 6300 Processors</td>
<td>128 256 512</td>
<td>3200 2933 2666 2400</td>
</tr>
<tr>
<td>Platinum 8300 processors</td>
<td>128 256 512</td>
<td>3200 2933 2666 2400</td>
</tr>
</tbody>
</table>

### Table 6. Standard DDR4 DIMMs Compatible with Intel® Optane™ Persistent Memory 200 Series Module

<table>
<thead>
<tr>
<th>Type</th>
<th>Ranks per DIMM and Data Width</th>
<th>8 Gb DRAM density</th>
<th>16 Gb DRAM density</th>
</tr>
</thead>
<tbody>
<tr>
<td>RDIMM (PTH – up to 2933 MT/s) (SMT – up to 3200 MT/s)</td>
<td>SR x8</td>
<td>N/A</td>
<td>N/A</td>
</tr>
<tr>
<td></td>
<td>SR x4</td>
<td>16</td>
<td>32</td>
</tr>
<tr>
<td></td>
<td>DR x8</td>
<td>16</td>
<td>32</td>
</tr>
<tr>
<td></td>
<td>DR x4</td>
<td>32</td>
<td>64</td>
</tr>
<tr>
<td>3DS-RDIMM (PTH – up to 2933 MT/s) (SMT – up to 3200 MT/s)</td>
<td>QR x4</td>
<td>N/A</td>
<td>128 (2H)</td>
</tr>
<tr>
<td></td>
<td>OR x4</td>
<td>N/A</td>
<td>256 (4H)</td>
</tr>
<tr>
<td>LRDIMM (PTH/SMT – up to 3200 MT/s)</td>
<td>QR x4</td>
<td>64</td>
<td>128</td>
</tr>
<tr>
<td>3DS-LRDIMM (PTH/SMT – up to 3200 MT/s)</td>
<td>QR x4</td>
<td>N/A</td>
<td>N/A</td>
</tr>
<tr>
<td></td>
<td>OR x4</td>
<td>N/A</td>
<td>256 (4H)</td>
</tr>
</tbody>
</table>

**Note:** SR = Single Rank, DR = Dual Rank, QR = Quad Rank, OR = Oct Rank, H = Stack Height, PTH = Plated Through Hole, SMT = Surface-Mount Technology
Appendix D. System Status LED Operating States and Definition

The server board includes a bi-color system status LED. The system status LED on the server board is tied directly to the system status LED on the front panel (if present). This LED indicates the current health of the server. Possible LED states include solid green, blinking green, solid amber, and blinking amber.

When the server is powered down (transitions to the DC-off state or S5), the BMC is still on standby power and retains the sensor and front panel status LED state established before the power-down event.

When AC power is first applied to the system, the status LED turns solid amber and then immediately changes to blinking green to indicate that the BMC is booting. If the BMC boot process completes with no errors, the status LED changes to solid green.

Table 7. System Status LED State Definitions

<table>
<thead>
<tr>
<th>LED State</th>
<th>System State</th>
<th>BIOS Status Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>No AC Power to system</td>
<td>• System power is not present.                                                                                                     • System is in EuP Lot6 off mode.</td>
</tr>
<tr>
<td>Solid green</td>
<td>System is operating normally.</td>
<td>• System is in S5 soft-off state.                                                                                                       • System is running (in S0 State) and its status is healthy. The system is not exhibiting any errors. Source power is present, BMC has booted, and manageability functionality is up and running.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• After a BMC reset, and with the chassis ID solid on, the BMC is booting Linux*. Control has been passed from BMC uBoot to BMC Linux*. The BMC in this state for roughly 10–20 seconds.</td>
</tr>
<tr>
<td>Blinking green</td>
<td>System is operating in a degraded state although still functioning, or system is operating in a redundant state but with an impending failure warning.</td>
<td>• Redundancy loss such as power-supply or fan. Applies only if the associated platform subsystem has redundancy capabilities.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Fan warning or failure when the number of fully operational fans is less than the minimum number needed to cool the system.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Non-critical threshold crossed – Temperature (including HSBP temp), voltage, input power to power supply, output current for main power rail from power supply and Processor Thermal Control (Therm Ctrl) sensors.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Power supply predictive failure occurred while redundant power supply configuration was present.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Unable to use all installed memory (more than 1 DIMM installed).                                                                      • Correctable Errors over a threshold and migrating to a spare DIMM (memory sparing). This indicates that the system no longer has spared DIMMs (a redundancy lost condition). Corresponding DIMM LED lit.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• In mirrored configuration, when memory mirroring takes place and system loses memory redundancy.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Battery failure.                                                                                                                     • BMC executing in uBoot. (Indicated by Chassis ID blinking at 3 Hz while Status blinking at 1 Hz). System in degraded state (no manageability). BMC uBoot is running but has not transferred control to BMC Linux*. Server will be in this state 6–8 seconds after BMC reset while it pulls the Linux* image into flash.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• BMC Watchdog has reset the BMC.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Power Unit sensor offset for configuration error is asserted.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• SSD Hot Swap Controller is off-line or degraded.</td>
</tr>
<tr>
<td>Blinking green and amber alternatively</td>
<td>System is initializing after source power is applied</td>
<td>• PFR in the process of updating/authenticating/recovering when source power is connected, system firmware being updated.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• System not ready to take power button event/signal.</td>
</tr>
<tr>
<td>LED State</td>
<td>System State</td>
<td>BIOS Status Description</td>
</tr>
<tr>
<td>---------------------</td>
<td>------------------------------------------------------------------------------</td>
<td>--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------</td>
</tr>
</tbody>
</table>
| Blinking amber      | System is operating in a degraded state with an impending failure warning, although still functioning. System is likely to fail. | • Critical threshold crossed – Voltage, temperature (including HSBP temp), input power to power supply, output current for main power rail from power supply and PROCHOT (Therm Ctrl) sensors.  
• VRD Hot asserted.  
• Minimum number of fans to cool the system not present or failed.  
• Hard drive fault.  
• Power Unit Redundancy sensor – Insufficient resources offset (indicates not enough power supplies present).  
• In non-sparing and non-mirroring mode, if the threshold of correctable errors is crossed within the window.  
• Invalid firmware image detected during boot up or firmware update. |
| Solid amber         | Critical/non-recoverable – system is halted. Fatal alarm – system has failed or shut down. | • Processor CATERR signal asserted.  
• MSID mismatch detected (CATERR also asserts for this case).  
• CPU 0 is missing.  
• Processor Thermal Trip.  
• No power good – power fault.  
• DIMM failure when there is only 1 DIMM present and hence no good memory present.  
• Runtime memory uncorrectable error in non-redundant mode.  
• DIMM Thermal Trip or equivalent.  
• SSB Thermal Trip or equivalent.  
• Processor ERR2 signal asserted.  
• BMC/Video memory test failed. (Chassis ID shows blue/solid-on for this condition.)  
• Both uBoot BMC firmware images are bad. (Chassis ID shows blue/solid-on for this condition.)  
• 240 VA fault.  
• Fatal Error in processor initialization:  
  o Processor family not identical  
  o Processor model not identical  
  o Processor core/thread counts not identical  
  o Processor cache size not identical  
  o Unable to synchronize processor frequency  
  o Unable to synchronize QPI link frequency  
• BMC fail authentication with non-recoverable condition, system hang at T-1; boot PCH only, system hang; PIT failed, system lockdown. |
Appendix E. POST Code Diagnostic LED Decoder

As an aid in troubleshooting a system hang that occurs during a system POST process, the server board includes a bank of eight POST code diagnostic LEDs on the back edge of the server board.

During the system boot process, Memory Reference Code (MRC) and system BIOS execute a number of memory initialization and platform configuration routines, each of which is assigned a hex POST code number.

As each process is started, the given POST code number is displayed to the POST code diagnostic LEDs on the back edge of the server board.

During a POST system hang, the displayed POST code can be used to identify the last POST routine that was run before the error occurred, helping to isolate the possible cause of the hang condition.

Each POST code is represented by eight LEDs, four green LEDs and four amber LEDs. The POST codes are divided into two nibbles, an upper nibble and a lower nibble. The upper nibble bits are represented by amber diagnostic LEDs and the lower nibble bits are represented by green diagnostics LEDs. If the bit is set, the corresponding LED is lit. If the bit is clear, the corresponding LED is off. For each set of nibble bits, LED 0 represents the least significant bit (LSB) and LED 3 represents the most significant bit (MSB).

![Figure 140. Onboard POST Diagnostic LEDs](image)

**Note:** Diagnostic LEDs are best read and decoded when viewing the LEDs from the back of the system.

In the following example, the BIOS sends a value of AC to the diagnostic LED decoder. The LEDs are decoded as shown in the following table.
Upper nibble bits = 1010b = Ah; Lower nibble bits = 1100b = Ch; the two Hex Nibble values are combined to create a single ACh POST Progress Code.

### E.1 Early POST Memory Initialization MRC Diagnostic Codes

Memory initialization at the beginning of POST includes multiple functions: discovery, channel training, validation that the DIMM population is acceptable and functional, initialization of the IMC and other hardware settings, and initialization of applicable RAS configurations.

The MRC progress codes are displayed to the diagnostic LEDs that show the execution point in the MRC operational path at each step.

#### Table 8. POST Progress Code LED Example

<table>
<thead>
<tr>
<th>LEDs</th>
<th>Upper Nibble AMBER LEDs</th>
<th>Lower Nibble GREEN LEDs</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>MSB</td>
<td>LSB</td>
</tr>
<tr>
<td></td>
<td>LED #7</td>
<td>LED #6</td>
</tr>
<tr>
<td></td>
<td>8h</td>
<td>4h</td>
</tr>
<tr>
<td>Status</td>
<td>ON</td>
<td>OFF</td>
</tr>
<tr>
<td>Read Value</td>
<td>Binary</td>
<td>1</td>
</tr>
<tr>
<td>Hexadecimal</td>
<td>Ah</td>
<td>Ch</td>
</tr>
<tr>
<td>Result</td>
<td>ACh</td>
<td></td>
</tr>
</tbody>
</table>

Should a major memory initialization error occur, preventing the system from booting with data integrity, a beep code is generated, the MRC displays a fatal error code on the diagnostic LEDs, and a system halt command is executed. Fatal MRC error halts do not change the state of the system status LED and they do not get logged as SEL events. **Table 10** lists all MRC fatal errors that are displayed to the diagnostic LEDs.
**Note:** Fatal MRC errors display POST error codes that may be the same as BIOS POST progress codes displayed later in the POST process. The fatal MRC codes can be distinguished from the BIOS POST progress codes by the accompanying memory failure beep code of three long beeps as identified in **Table 10**.

**Table 10. MRC Fatal Error Codes**

<table>
<thead>
<tr>
<th>Post Code (Hex)</th>
<th>Upper Nibble</th>
<th>Lower Nibble</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>EB 11101000</td>
<td></td>
<td></td>
<td>No usable memory error 01h = No memory was detected from SPD read, or invalid config that causes no operable memory. 02h = Memory DIMMs on all channels of all sockets are disabled due to hardware memtest error. 03h = No memory installed. All channels are disabled.</td>
</tr>
<tr>
<td>E9 11101001</td>
<td></td>
<td></td>
<td>Memory is locked by Intel® TXT and is inaccessible</td>
</tr>
<tr>
<td>EA 11101010</td>
<td></td>
<td></td>
<td>DDR4 channel training error 01h = Error on read DQ/DQS (Data/Data Strobe) init 02h = Error on Receive Enable 03h = Error on Write Leveling 04h = Error on write DQ/DQS (Data/Data Strobe)</td>
</tr>
<tr>
<td>EB 11101111</td>
<td></td>
<td></td>
<td>Memory test failure 01h = Software memtest failure. 02h = Hardware memtest failed.</td>
</tr>
<tr>
<td>ED 11101101</td>
<td></td>
<td></td>
<td>DIMM configuration population error 01h = Different DIMM types (RDIMM, LRDIMM) are detected installed in the system. 02h = Violation of DIMM population rules. 03h = The 3rd DIMM slot cannot be populated when QR DIMMs are installed. 04h = UDIMMs are not supported. 05h = Unsupported DIMM Voltage.</td>
</tr>
<tr>
<td>EF 11101111</td>
<td></td>
<td></td>
<td>Indicates a CLTT table structure error</td>
</tr>
</tbody>
</table>
## BIOS POST Progress Codes

The following table provides a list of all POST progress codes.

### Table 11. POST Progress Codes

<table>
<thead>
<tr>
<th>Post Code (Hex)</th>
<th>Upper Nibble</th>
<th>Lower Nibble</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Security (SEC) Phase</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>01</td>
<td>0 0 0 0</td>
<td>0 0 0 1</td>
<td>First POST code after CPU reset</td>
</tr>
<tr>
<td>02</td>
<td>0 0 0 0</td>
<td>0 0 1 0</td>
<td>Microcode load begin</td>
</tr>
<tr>
<td>03</td>
<td>0 0 0 0</td>
<td>0 0 1 1</td>
<td>CRAM initialization begin</td>
</tr>
<tr>
<td>04</td>
<td>0 0 0 0</td>
<td>0 1 0 0</td>
<td>PEI Cache When Disabled</td>
</tr>
<tr>
<td>05</td>
<td>0 0 0 0</td>
<td>0 1 0 1</td>
<td>SEC Core At Power On Begin.</td>
</tr>
<tr>
<td>06</td>
<td>0 0 0 0</td>
<td>0 1 1 0</td>
<td>Early CPU initialization during SEC Phase.</td>
</tr>
<tr>
<td>Intel® UPI RC (Fully leverage without platform change)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>A1</td>
<td>1 0 1 0</td>
<td>0 0 0 1</td>
<td>Collect info such as SBSP, boot mode, reset type, etc.</td>
</tr>
<tr>
<td>A3</td>
<td>1 0 1 0</td>
<td>0 0 1 1</td>
<td>Setup minimum path between SBSP and other sockets</td>
</tr>
<tr>
<td>A6</td>
<td>1 0 1 0</td>
<td>0 1 1 0</td>
<td>Sync up with PBSPs</td>
</tr>
<tr>
<td>A7</td>
<td>1 0 1 0</td>
<td>0 1 1 1</td>
<td>Topology discovery and route calculation</td>
</tr>
<tr>
<td>A8</td>
<td>1 0 1 0</td>
<td>1 0 0 0</td>
<td>Program final route</td>
</tr>
<tr>
<td>A9</td>
<td>1 0 1 0</td>
<td>1 0 0 1</td>
<td>Program final IO SAD setting</td>
</tr>
<tr>
<td>AA</td>
<td>1 0 1 0</td>
<td>1 0 1 0</td>
<td>Protocol layer and other uncore settings</td>
</tr>
<tr>
<td>AB</td>
<td>1 0 1 0</td>
<td>1 0 1 1</td>
<td>Transition links to full speed operation</td>
</tr>
<tr>
<td>AE</td>
<td>1 0 1 0</td>
<td>1 1 1 0</td>
<td>Coherency settings</td>
</tr>
<tr>
<td>AF</td>
<td>1 0 1 0</td>
<td>1 1 1 1</td>
<td>KTI initialization done</td>
</tr>
<tr>
<td>Pre-EFI Initialization (PEI) Phase</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>0 0 0 1</td>
<td>0 0 0 0</td>
<td>PEI Core</td>
</tr>
<tr>
<td>11</td>
<td>0 0 0 1</td>
<td>0 0 0 1</td>
<td>CPU PEIM</td>
</tr>
<tr>
<td>15</td>
<td>0 0 0 1</td>
<td>0 1 0 1</td>
<td>Platform Type Init</td>
</tr>
<tr>
<td>19</td>
<td>0 0 0 1</td>
<td>1 0 0 1</td>
<td>Platform PEIM Init</td>
</tr>
<tr>
<td>Integrated I/O (IIO) Progress Codes</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>E0</td>
<td>1 1 1 0</td>
<td>0 0 0 0</td>
<td>IIO Early Init Entry</td>
</tr>
<tr>
<td>E1</td>
<td>1 1 1 0</td>
<td>0 0 0 1</td>
<td>IIO Pre-link Training</td>
</tr>
<tr>
<td>E2</td>
<td>1 1 1 0</td>
<td>0 1 0 0</td>
<td>IIO EQ Programming</td>
</tr>
<tr>
<td>E3</td>
<td>1 1 1 0</td>
<td>0 0 1 1</td>
<td>IIO Link Training</td>
</tr>
<tr>
<td>E4</td>
<td>1 1 1 0</td>
<td>0 1 0 0</td>
<td>Internal Use</td>
</tr>
<tr>
<td>E5</td>
<td>1 1 1 0</td>
<td>0 1 0 1</td>
<td>IIO Early Init Exit</td>
</tr>
<tr>
<td>E6</td>
<td>1 1 1 0</td>
<td>0 1 1 0</td>
<td>IIO Late Init Entry</td>
</tr>
<tr>
<td>E7</td>
<td>1 1 1 0</td>
<td>0 1 1 1</td>
<td>IIO PCIe Ports Init</td>
</tr>
<tr>
<td>E8</td>
<td>1 1 1 0</td>
<td>1 0 0 0</td>
<td>IIO IOAPIC init</td>
</tr>
<tr>
<td>E9</td>
<td>1 1 1 0</td>
<td>1 0 0 1</td>
<td>IIO VTD Init</td>
</tr>
<tr>
<td>EA</td>
<td>1 1 1 0</td>
<td>1 0 1 0</td>
<td>IIO IOAT Init</td>
</tr>
<tr>
<td>EB</td>
<td>1 1 1 0</td>
<td>1 0 1 1</td>
<td>IIO DXF Init</td>
</tr>
<tr>
<td>EC</td>
<td>1 1 1 0</td>
<td>1 1 0 0</td>
<td>IIO NTB Init</td>
</tr>
<tr>
<td>ED</td>
<td>1 1 1 0</td>
<td>1 1 0 1</td>
<td>IIO Security Init</td>
</tr>
<tr>
<td>EE</td>
<td>1 1 1 0</td>
<td>1 1 1 0</td>
<td>IIO Late Init Exit</td>
</tr>
<tr>
<td>EF</td>
<td>1 1 1 0</td>
<td>1 1 1 1</td>
<td>IIO ready to boot</td>
</tr>
</tbody>
</table>
### Post Code (Hex) at this point the MRC Progress Code Sequence is Executed.

<table>
<thead>
<tr>
<th>Post Code</th>
<th>Upper Nibble</th>
<th>Lower Nibble</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>31</td>
<td>0 0 1 1</td>
<td>0 0 0 1</td>
<td>Memory Installed</td>
</tr>
<tr>
<td>32</td>
<td>0 0 1 1</td>
<td>0 0 1 0</td>
<td>CPU PEIM (CPU init)</td>
</tr>
<tr>
<td>33</td>
<td>0 0 1 1</td>
<td>0 0 1 1</td>
<td>CPU PEIM (Cache Init)</td>
</tr>
<tr>
<td>34</td>
<td>0 0 1 1</td>
<td>0 1 0 0</td>
<td>CPU BSP Select</td>
</tr>
<tr>
<td>35</td>
<td>0 0 1 1</td>
<td>0 1 0 1</td>
<td>CPU AP Init</td>
</tr>
<tr>
<td>36</td>
<td>0 0 1 1</td>
<td>0 1 1 0</td>
<td>CPU SMM Init</td>
</tr>
<tr>
<td>4F</td>
<td>0 1 0 0</td>
<td>1 1 1 1</td>
<td>DXE IPL started</td>
</tr>
</tbody>
</table>

### MRC Progress Codes

- **MRC Progress Codes** – At this point the MRC Progress Code Sequence is Executed.

### Memory Feature Progress Codes

<table>
<thead>
<tr>
<th>Code</th>
<th>Upper Nibble</th>
<th>Lower Nibble</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1</td>
<td>1 1 0 0</td>
<td>0 0 0 1</td>
<td>Memory POR check</td>
</tr>
<tr>
<td>C2</td>
<td>1 1 0 0</td>
<td>0 0 1 0</td>
<td>Internal Use</td>
</tr>
<tr>
<td>C3</td>
<td>1 1 0 0</td>
<td>0 0 1 1</td>
<td>Internal Use</td>
</tr>
<tr>
<td>C4</td>
<td>1 1 0 0</td>
<td>0 1 0 0</td>
<td>Internal Use</td>
</tr>
<tr>
<td>C5</td>
<td>1 1 0 0</td>
<td>0 1 0 1</td>
<td>Memory Early Init</td>
</tr>
<tr>
<td>C6</td>
<td>1 1 0 0</td>
<td>0 1 1 0</td>
<td>Display DIMM info in debug mode</td>
</tr>
<tr>
<td>C7</td>
<td>1 1 0 0</td>
<td>0 1 1 1</td>
<td>JEDEC Nvdimm training</td>
</tr>
<tr>
<td>C9</td>
<td>1 1 0 0</td>
<td>1 0 0 1</td>
<td>Setup SVL and Scrambling</td>
</tr>
<tr>
<td>CA</td>
<td>1 1 0 0</td>
<td>1 0 1 0</td>
<td>Internal Use</td>
</tr>
<tr>
<td>CB</td>
<td>1 1 0 0</td>
<td>1 0 1 1</td>
<td>Check RAS support</td>
</tr>
<tr>
<td>CC</td>
<td>1 1 0 0</td>
<td>1 1 0 0</td>
<td>Pmem ADR Init</td>
</tr>
<tr>
<td>CD</td>
<td>1 1 0 0</td>
<td>1 1 0 1</td>
<td>Internal Use</td>
</tr>
<tr>
<td>CE</td>
<td>1 1 0 0</td>
<td>1 1 1 0</td>
<td>Memory Late Init</td>
</tr>
<tr>
<td>CF</td>
<td>1 1 0 0</td>
<td>1 1 1 1</td>
<td>Determine MRC boot mode</td>
</tr>
<tr>
<td>D0</td>
<td>1 1 0 1</td>
<td>0 0 0 0</td>
<td>MKTME Early Init</td>
</tr>
<tr>
<td>D1</td>
<td>1 1 0 1</td>
<td>0 0 0 1</td>
<td>SGX Early Init</td>
</tr>
<tr>
<td>D2</td>
<td>1 1 0 1</td>
<td>0 0 1 0</td>
<td>Memory Margin Test</td>
</tr>
<tr>
<td>D3</td>
<td>1 1 0 1</td>
<td>0 0 1 1</td>
<td>Internal Use</td>
</tr>
<tr>
<td>D5</td>
<td>1 1 0 1</td>
<td>0 1 0 1</td>
<td>Internal Use</td>
</tr>
<tr>
<td>D6</td>
<td>1 1 0 1</td>
<td>0 1 1 0</td>
<td>Offset Training Result</td>
</tr>
</tbody>
</table>

### Driver Execution Environment (DXE) Phase

<table>
<thead>
<tr>
<th>Code</th>
<th>Upper Nibble</th>
<th>Lower Nibble</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>60</td>
<td>0 1 1 0</td>
<td>0 0 0 0</td>
<td>DXE Core started</td>
</tr>
<tr>
<td>62</td>
<td>0 1 1 0</td>
<td>0 0 1 0</td>
<td>DXE Setup Init</td>
</tr>
<tr>
<td>68</td>
<td>0 1 1 0</td>
<td>1 0 0 0</td>
<td>DXE PCI Host Bridge Init</td>
</tr>
<tr>
<td>69</td>
<td>0 1 1 0</td>
<td>1 0 0 1</td>
<td>DXE NB Init</td>
</tr>
<tr>
<td>6A</td>
<td>0 1 1 0</td>
<td>1 0 1 0</td>
<td>DXE NB SMM Init</td>
</tr>
<tr>
<td>70</td>
<td>0 1 1 1</td>
<td>0 0 0 0</td>
<td>DXE SB Init</td>
</tr>
<tr>
<td>71</td>
<td>0 1 1 1</td>
<td>0 0 0 1</td>
<td>DXE SB SMM Init</td>
</tr>
<tr>
<td>72</td>
<td>0 1 1 1</td>
<td>0 0 1 0</td>
<td>DXE SB devices Init</td>
</tr>
<tr>
<td>78</td>
<td>0 1 1 1</td>
<td>1 0 0 0</td>
<td>DXE ACPI Init</td>
</tr>
<tr>
<td>79</td>
<td>0 1 1 1</td>
<td>1 0 0 1</td>
<td>DXE CSM Init</td>
</tr>
<tr>
<td>7D</td>
<td>0 1 1 1</td>
<td>1 1 0 1</td>
<td>DXE Removable Media Detect</td>
</tr>
<tr>
<td>7E</td>
<td>0 1 1 1</td>
<td>1 1 1 0</td>
<td>DXE Removable Media Detected</td>
</tr>
<tr>
<td>90</td>
<td>1 0 0 1</td>
<td>0 0 0 0</td>
<td>DXE BDS started</td>
</tr>
<tr>
<td>91</td>
<td>1 0 0 1</td>
<td>0 0 0 1</td>
<td>DXE BDS connect drivers</td>
</tr>
<tr>
<td>92</td>
<td>1 0 0 1</td>
<td>0 0 1 0</td>
<td>DXE PCI bus begin</td>
</tr>
<tr>
<td>Post Code (Hex)</td>
<td>Upper Nibble</td>
<td>Lower Nibble</td>
<td>Description</td>
</tr>
<tr>
<td>-----------------</td>
<td>--------------</td>
<td>--------------</td>
<td>---------------------------</td>
</tr>
<tr>
<td>93</td>
<td>1 0 0 1</td>
<td>0 1 1 1</td>
<td>DXE PCI Bus HPC Init</td>
</tr>
<tr>
<td>94</td>
<td>1 0 0 1</td>
<td>0 1 0 0</td>
<td>DXE PCI Bus enumeration</td>
</tr>
<tr>
<td>95</td>
<td>1 0 0 1</td>
<td>0 1 0 1</td>
<td>DXE PCI Bus resource requested</td>
</tr>
<tr>
<td>96</td>
<td>1 0 0 1</td>
<td>0 1 1 0</td>
<td>DXE PCI Bus assign resource</td>
</tr>
<tr>
<td>97</td>
<td>1 0 0 1</td>
<td>0 1 1 1</td>
<td>DXE CON_OUT connect</td>
</tr>
<tr>
<td>98</td>
<td>1 0 0 1</td>
<td>1 0 1 1</td>
<td>DXE CON_IN connect</td>
</tr>
<tr>
<td>99</td>
<td>1 0 0 1</td>
<td>1 0 1 0</td>
<td>DXE SIO Init</td>
</tr>
<tr>
<td>9A</td>
<td>1 0 0 1</td>
<td>1 0 1 0</td>
<td>DXE USB start</td>
</tr>
<tr>
<td>9B</td>
<td>1 0 0 1</td>
<td>1 0 1 1</td>
<td>DXE USB reset</td>
</tr>
<tr>
<td>9C</td>
<td>1 0 0 1</td>
<td>1 1 0 0</td>
<td>DXE USB detect</td>
</tr>
<tr>
<td>9D</td>
<td>1 0 0 1</td>
<td>1 1 0 1</td>
<td>DXE USB enable</td>
</tr>
<tr>
<td>A1</td>
<td>1 0 1 0</td>
<td>0 0 0 1</td>
<td>DXE IDE begin</td>
</tr>
<tr>
<td>A2</td>
<td>1 0 1 0</td>
<td>0 0 1 0</td>
<td>DXE IDE reset</td>
</tr>
<tr>
<td>A3</td>
<td>1 0 1 0</td>
<td>0 0 1 1</td>
<td>DXE IDE detect</td>
</tr>
<tr>
<td>A4</td>
<td>1 0 1 0</td>
<td>0 1 0 0</td>
<td>DXE IDE enable</td>
</tr>
<tr>
<td>A5</td>
<td>1 0 1 0</td>
<td>0 1 0 1</td>
<td>DXE SCSI begin</td>
</tr>
<tr>
<td>A6</td>
<td>1 0 1 0</td>
<td>0 1 1 0</td>
<td>DXE SCSI reset</td>
</tr>
<tr>
<td>A7</td>
<td>1 0 1 0</td>
<td>0 1 1 1</td>
<td>DXE SCSI detect</td>
</tr>
<tr>
<td>A8</td>
<td>1 0 1 0</td>
<td>1 0 0 0</td>
<td>DXE SCSI enable</td>
</tr>
<tr>
<td>A9</td>
<td>1 0 1 0</td>
<td>1 0 1 1</td>
<td>DXE SETUP start</td>
</tr>
<tr>
<td>AC</td>
<td>1 0 1 0</td>
<td>1 1 0 0</td>
<td>DXE SETUP input wait</td>
</tr>
<tr>
<td>AD</td>
<td>1 0 1 0</td>
<td>1 1 0 1</td>
<td>DXE Ready to Boot</td>
</tr>
<tr>
<td>AE</td>
<td>1 0 1 0</td>
<td>1 1 1 0</td>
<td>DXE Legacy Boot</td>
</tr>
<tr>
<td>AF</td>
<td>1 0 1 0</td>
<td>1 1 1 1</td>
<td>DXE Exit Boot Services</td>
</tr>
<tr>
<td>B0</td>
<td>1 0 1 1</td>
<td>0 0 0 0</td>
<td>RT Set Virtual Address Map Begin</td>
</tr>
<tr>
<td>B1</td>
<td>1 0 1 1</td>
<td>0 0 0 1</td>
<td>RT Set Virtual Address Map End</td>
</tr>
<tr>
<td>B2</td>
<td>1 0 1 1</td>
<td>0 0 1 0</td>
<td>DXE Legacy Option ROM init</td>
</tr>
<tr>
<td>B3</td>
<td>1 0 1 1</td>
<td>0 0 1 1</td>
<td>DXE Reset system</td>
</tr>
<tr>
<td>B4</td>
<td>1 0 1 1</td>
<td>0 1 0 0</td>
<td>DXE USB Hot plug</td>
</tr>
<tr>
<td>B5</td>
<td>1 0 1 1</td>
<td>0 1 0 1</td>
<td>DXE PCI BUS Hot plug</td>
</tr>
<tr>
<td>B6</td>
<td>1 0 1 1</td>
<td>1 0 0 0</td>
<td>PWRBTN Shutdown</td>
</tr>
<tr>
<td>B7</td>
<td>1 0 1 1</td>
<td>1 0 0 1</td>
<td>SLEEP Shutdown</td>
</tr>
<tr>
<td>C0</td>
<td>1 1 1 1</td>
<td>0 0 0 0</td>
<td>End of DXE</td>
</tr>
<tr>
<td>C7</td>
<td>1 1 1 0</td>
<td>0 1 1 1</td>
<td>DXE ACPI Enable</td>
</tr>
<tr>
<td>0</td>
<td>0 0 0 0</td>
<td>0 0 0 0</td>
<td>Clear POST Code</td>
</tr>
</tbody>
</table>

**S3 Resume**

<table>
<thead>
<tr>
<th>Post Code (Hex)</th>
<th>Upper Nibble</th>
<th>Lower Nibble</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>E0</td>
<td>1 1 1 0</td>
<td>0 0 0 0</td>
<td>S3 Resume PEIM (S3 started)</td>
</tr>
<tr>
<td>E1</td>
<td>1 1 1 0</td>
<td>0 0 0 1</td>
<td>S3 Resume PEIM (S3 boot script)</td>
</tr>
<tr>
<td>E2</td>
<td>1 1 1 0</td>
<td>0 0 1 0</td>
<td>S3 Resume PEIM (S3 Video Repost)</td>
</tr>
<tr>
<td>E3</td>
<td>1 1 1 0</td>
<td>0 0 1 1</td>
<td>S3 Resume PEIM (S3 OS wake)</td>
</tr>
</tbody>
</table>
Appendix F. POST Code Errors

Most error conditions encountered during POST are reported using POST error codes. These codes represent specific failures, warnings, or information. POST error codes may be displayed in the error manager display screen and are always logged to the System Event Log (SEL). Logged events are available to system management applications, including remote and Out of Band (OOB) management.

There are exception cases in early initialization where system resources are not adequately initialized for handling POST Error Code reporting. These cases are primarily fatal error conditions resulting from initialization of processors and memory, and they are handed by a diagnostic LED display with a system halt.

Table 12 lists the supported POST error codes. Each error code is assigned an error type that determines the action the BIOS takes when the error is encountered. Error types include minor, major, and fatal. The BIOS action for each is defined as follows:

- **Minor**: An error message may be displayed to the screen or to the BIOS Setup Error Manager and the POST error code is logged to the SEL. The system continues booting in a degraded state. The user may want to replace the erroneous unit. The “POST Error Pause” option setting in the BIOS Setup does not affect this error.

- **Major**: An error message is displayed to the Error Manager screen and an error is logged to the SEL. If the BIOS Setup option “Post Error Pause” is enabled, operator intervention is required to continue booting the system. If the BIOS Setup option “POST Error Pause” is disabled, the system continues to boot.

**Note**: For 0048 "Password check failed", the system halts and then, after the next reset/reboot, displays the error code on the Error Manager screen.

- **Fatal**: If the system cannot boot, POST halts and displays the following message:

  Unrecoverable fatal error found. System will not boot until the error is resolved. Press <F2> to enter setup.

  When the <F2> key on the keyboard is pressed, the error message is displayed on the Error Manager screen and an error is logged to the system event log (SEL) with the POST error code. The system cannot boot unless the error is resolved. The faulty component must be replaced. The “POST Error Pause” option setting in the BIOS Setup does not affect this error.

**Note**: The POST error codes in the following table are common to all current generation Intel® server platforms. Features present on a given server board/system determine which of the listed error codes are supported.
<table>
<thead>
<tr>
<th>Error Code</th>
<th>Error Message</th>
<th>Action message</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>0012</td>
<td>System RTC date/time not set</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>0048</td>
<td>Password check failed</td>
<td>Put right password.</td>
<td>Major</td>
</tr>
<tr>
<td>0140</td>
<td>PCI component encountered a PERR error</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>0141</td>
<td>PCI resource conflict</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>0146</td>
<td>PCI out of resources error</td>
<td>Enable Memory Mapped I/O above 4 GB item at SETUP to use 64-bit MMIO.</td>
<td>Major</td>
</tr>
<tr>
<td>0191</td>
<td>Processor core/thread count mismatch detected</td>
<td>Use identical CPU type.</td>
<td>Fatal</td>
</tr>
<tr>
<td>0192</td>
<td>Processor cache size mismatch detected</td>
<td>Use identical CPU type.</td>
<td>Fatal</td>
</tr>
<tr>
<td>0194</td>
<td>Processor family mismatch detected</td>
<td>Use identical CPU type.</td>
<td>Fatal</td>
</tr>
<tr>
<td>0195</td>
<td>Processor Intel(R) UPI link frequencies unable to synchronize</td>
<td></td>
<td>Fatal</td>
</tr>
<tr>
<td>0196</td>
<td>Processor model mismatch detected</td>
<td>Use identical CPU type.</td>
<td>Fatal</td>
</tr>
<tr>
<td>0197</td>
<td>Processor frequencies unable to synchronize</td>
<td>Use identical CPU type.</td>
<td>Fatal</td>
</tr>
<tr>
<td>5220</td>
<td>BIOS Settings reset to default settings</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>5221</td>
<td>Passwords cleared by jumper</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>5224</td>
<td>Password clear jumper is Set</td>
<td>Recommend reminding user to install BIOS password as BIOS admin password is the master keys for several BIOS security features.</td>
<td>Major</td>
</tr>
<tr>
<td>8130</td>
<td>CPU 0 disabled</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8131</td>
<td>CPU 1 disabled</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8160</td>
<td>CPU 0 unable to apply microcode update</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8161</td>
<td>CPU 1 unable to apply microcode update</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8170</td>
<td>CPU 0 failed Self-Test (BIST)</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8171</td>
<td>CPU 1 failed Self-Test (BIST)</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8180</td>
<td>CPU 0 microcode update not found</td>
<td></td>
<td>Minor</td>
</tr>
<tr>
<td>8181</td>
<td>CPU 1 microcode update not found</td>
<td></td>
<td>Minor</td>
</tr>
<tr>
<td>8190</td>
<td>Watchdog timer failed on last boot.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8198</td>
<td>OS boot watchdog timer failure.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8300</td>
<td>Baseboard Management Controller failed self-test.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8305</td>
<td>Hot Swap Controller failure</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>83A0</td>
<td>Management Engine (ME) failed self-test.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>83A1</td>
<td>Management Engine (ME) Failed to respond.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>84F2</td>
<td>Baseboard management controller failed to respond</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>84F3</td>
<td>Baseboard Management Controller in Update Mode.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>84F4</td>
<td>Baseboard Management Controller Sensor Data Record empty.</td>
<td>Update right SDR.</td>
<td>Major</td>
</tr>
<tr>
<td>84FF</td>
<td>System Event Log full</td>
<td>Clear SEL through EWS or SELVIEW utility.</td>
<td>Minor</td>
</tr>
<tr>
<td>85FC</td>
<td>Memory component could not be configured in the selected RAS mode</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8501</td>
<td>Memory Population Error</td>
<td>Plug DIMM at right population.</td>
<td>Major</td>
</tr>
<tr>
<td>8502</td>
<td>PMem invalid DIMM population found on the system.</td>
<td>Populate valid POR PMem DIMM population.</td>
<td>Major</td>
</tr>
<tr>
<td>8520</td>
<td>Memory failed test/initialization CPU0_DIMM_A1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8521</td>
<td>Memory failed test/initialization CPU0_DIMM_A2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8522</td>
<td>Memory failed test/initialization CPU0_DIMM_A3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>Error Code</td>
<td>Error Message</td>
<td>Action message</td>
<td>Type</td>
</tr>
<tr>
<td>------------</td>
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<td>----------------</td>
<td>------</td>
</tr>
<tr>
<td>8523</td>
<td>Memory failed test/initialization CPU0_DIMM_B1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8524</td>
<td>Memory failed test/initialization CPU0_DIMM_B2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8525</td>
<td>Memory failed test/initialization CPU0_DIMM_B3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8526</td>
<td>Memory failed test/initialization CPU0_DIMM_C1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8527</td>
<td>Memory failed test/initialization CPU0_DIMM_C2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8528</td>
<td>Memory failed test/initialization CPU0_DIMM_C3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8529</td>
<td>Memory failed test/initialization CPU0_DIMM_D1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>852A</td>
<td>Memory failed test/initialization CPU0_DIMM_D2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>852B</td>
<td>Memory failed test/initialization CPU0_DIMM_D3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>852C</td>
<td>Memory failed test/initialization CPU0_DIMM_E1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>852D</td>
<td>Memory failed test/initialization CPU0_DIMM_E2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>852E</td>
<td>Memory failed test/initialization CPU0_DIMM_E3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>852F</td>
<td>Memory failed test/initialization CPU0_DIMM_F1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8530</td>
<td>Memory failed test/initialization CPU0_DIMM_F2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
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<td>8531</td>
<td>Memory failed test/initialization CPU0_DIMM_F3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8532</td>
<td>Memory failed test/initialization CPU0_DIMM_G1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8533</td>
<td>Memory failed test/initialization CPU0_DIMM_G2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8534</td>
<td>Memory failed test/initialization CPU0_DIMM_G3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8535</td>
<td>Memory failed test/initialization CPU0_DIMM_H1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8536</td>
<td>Memory failed test/initialization CPU0_DIMM_H2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8537</td>
<td>Memory failed test/initialization CPU0_DIMM_H3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8538</td>
<td>Memory failed test/initialization CPU1_DIMM_A1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8539</td>
<td>Memory failed test/initialization CPU1_DIMM_A2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>853A</td>
<td>Memory failed test/initialization CPU1_DIMM_A3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>853B</td>
<td>Memory failed test/initialization CPU1_DIMM_B1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>853C</td>
<td>Memory failed test/initialization CPU1_DIMM_B2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>853D</td>
<td>Memory failed test/initialization CPU1_DIMM_B3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>853E</td>
<td>Memory failed test/initialization CPU1_DIMM_C1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>853F</td>
<td>Memory failed test/initialization CPU1_DIMM_C2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8540</td>
<td>Memory disabled.CPU0_DIMM_A1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8541</td>
<td>Memory disabled.CPU0_DIMM_A2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8542</td>
<td>Memory disabled.CPU0_DIMM_A3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8543</td>
<td>Memory disabled.CPU0_DIMM_B1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8544</td>
<td>Memory disabled.CPU0_DIMM_B2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8545</td>
<td>Memory disabled.CPU0_DIMM_B3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8546</td>
<td>Memory disabled.CPU0_DIMM_C1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8547</td>
<td>Memory disabled.CPU0_DIMM_C2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8548</td>
<td>Memory disabled.CPU0_DIMM_C3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8549</td>
<td>Memory disabled.CPU0_DIMM_D1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>854A</td>
<td>Memory disabled.CPU0_DIMM_D2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>854B</td>
<td>Memory disabled.CPU0_DIMM_D3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>854C</td>
<td>Memory disabled.CPU0_DIMM_E1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>854D</td>
<td>Memory disabled.CPU0_DIMM_E2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>854E</td>
<td>Memory disabled.CPU0_DIMM_E3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>854F</td>
<td>Memory disabled.CPU0_DIMM_F1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>Error Code</td>
<td>Error Message</td>
<td>Action Message</td>
<td>Type</td>
</tr>
<tr>
<td>------------</td>
<td>-------------------------------------</td>
<td>---------------------------------</td>
<td>--------</td>
</tr>
<tr>
<td>8550</td>
<td>Memory disabled.CPU0_DIMM_F2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8551</td>
<td>Memory disabled.CPU0_DIMM_F3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8552</td>
<td>Memory disabled.CPU0_DIMM_G1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8553</td>
<td>Memory disabled.CPU0_DIMM_G2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8554</td>
<td>Memory disabled.CPU0_DIMM_G3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8555</td>
<td>Memory disabled.CPU0_DIMM_H1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8556</td>
<td>Memory disabled.CPU0_DIMM_H2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8557</td>
<td>Memory disabled.CPU0_DIMM_H3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8558</td>
<td>Memory disabled.CPU1_DIMM_A1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8559</td>
<td>Memory disabled.CPU1_DIMM_A2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>855A</td>
<td>Memory disabled.CPU1_DIMM_A3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>855B</td>
<td>Memory disabled.CPU1_DIMM_B1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>855C</td>
<td>Memory disabled.CPU1_DIMM_B2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>855D</td>
<td>Memory disabled.CPU1_DIMM_B3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>855E</td>
<td>Memory disabled.CPU1_DIMM_C1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>855F</td>
<td>Memory disabled.CPU1_DIMM_C2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>855F</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_A1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8560</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_A2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8561</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_A3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8562</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_B1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8563</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_B2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8564</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_B3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8565</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_C1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8566</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_C2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8567</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_C3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8568</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_D1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>8569</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_D2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>856A</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_D3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>856B</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_E1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>856C</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_E2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>856D</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_E3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>856E</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU0_DIMM_F1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>Error Code</td>
<td>Error Message</td>
<td>Action message</td>
<td>Type</td>
</tr>
<tr>
<td>------------</td>
<td>---------------</td>
<td>----------------</td>
<td>------</td>
</tr>
<tr>
<td>8570</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU0_DIMM_F2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8571</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU0_DIMM_F3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8572</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU0_DIMM_G1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8573</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU0_DIMM_G2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8574</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU0_DIMM_G3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8575</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU0_DIMM_H1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8576</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU0_DIMM_H2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8577</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU0_DIMM_H3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8578</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU1_DIMM_A1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8579</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU1_DIMM_A2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>857A</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU1_DIMM_A3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>857B</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU1_DIMM_B1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>857C</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU1_DIMM_B2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>857D</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU1_DIMM_B3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>857E</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU1_DIMM_C1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>857F</td>
<td>Memory encountered a Serial Presence Detection(SPD) failure.CPU1_DIMM_C2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C0</td>
<td>Memory failed test/initialization CPU1_DIMM_C3 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C1</td>
<td>Memory failed test/initialization CPU1_DIMM_D1 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C2</td>
<td>Memory failed test/initialization CPU1_DIMM_D2 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C3</td>
<td>Memory failed test/initialization CPU1_DIMM_D3 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C4</td>
<td>Memory failed test/initialization CPU1_DIMM_E1 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C5</td>
<td>Memory failed test/initialization CPU1_DIMM_E2 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C6</td>
<td>Memory failed test/initialization CPU1_DIMM_E3 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C7</td>
<td>Memory failed test/initialization CPU1_DIMM_F1 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C8</td>
<td>Memory failed test/initialization CPU1_DIMM_F2 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85C9</td>
<td>Memory failed test/initialization CPU1_DIMM_F3 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85CA</td>
<td>Memory failed test/initialization CPU1_DIMM_G1 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85CB</td>
<td>Memory failed test/initialization CPU1_DIMM_G2 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85CC</td>
<td>Memory failed test/initialization CPU1_DIMM_G3 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85CD</td>
<td>Memory failed test/initialization CPU1_DIMM_H1 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85CE</td>
<td>Memory failed test/initialization CPU1_DIMM_H2 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85CF</td>
<td>Memory failed test/initialization CPU1_DIMM_H3 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85D0</td>
<td>Memory disabled.CPU1_DIMM_C3 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85D1</td>
<td>Memory disabled.CPU1_DIMM_D1 Remove the disabled DIMM.</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>Error Code</td>
<td>Error Message</td>
<td>Action message</td>
<td>Type</td>
</tr>
<tr>
<td>------------</td>
<td>---------------</td>
<td>----------------</td>
<td>------</td>
</tr>
<tr>
<td>85D2</td>
<td>Memory disabled.CPU1_DIMM_D2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85D3</td>
<td>Memory disabled.CPU1_DIMM_D3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85D4</td>
<td>Memory disabled.CPU1_DIMM_E1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85D5</td>
<td>Memory disabled.CPU1_DIMM_E2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85D6</td>
<td>Memory disabled.CPU1_DIMM_E3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85D7</td>
<td>Memory disabled.CPU1_DIMM_F1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85D8</td>
<td>Memory disabled.CPU1_DIMM_F2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85D9</td>
<td>Memory disabled.CPU1_DIMM_F3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85DA</td>
<td>Memory disabled.CPU1_DIMM_G1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85DB</td>
<td>Memory disabled.CPU1_DIMM_G2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85DC</td>
<td>Memory disabled.CPU1_DIMM_G3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85DD</td>
<td>Memory disabled.CPU1_DIMM_H1</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85DE</td>
<td>Memory disabled.CPU1_DIMM_H2</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85DF</td>
<td>Memory disabled.CPU1_DIMM_H3</td>
<td>Remove the disabled DIMM.</td>
<td>Major</td>
</tr>
<tr>
<td>85E0</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_C3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E1</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_D1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E2</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_D2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E3</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_D3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E4</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_E1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E5</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_E2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E6</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_E3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E7</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_F1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E8</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_F2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85E9</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_F3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85EA</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_G1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85EB</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure. CPU1_DIMM_G2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85EC</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_G3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85ED</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_H1</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85EE</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_H2</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>85EF</td>
<td>Memory encountered a Serial Presence Detection (SPD) failure.CPU1_DIMM_H3</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8604</td>
<td>POST Reclaim of non-critical NVRAM variables</td>
<td></td>
<td>Minor</td>
</tr>
<tr>
<td>8605</td>
<td>BIOS Settings are corrupted</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>8606</td>
<td>NVRAM variable space was corrupted and has been reinitialized</td>
<td></td>
<td>Major</td>
</tr>
</tbody>
</table>
### Error Code

<table>
<thead>
<tr>
<th>Error Code</th>
<th>Error Message</th>
<th>Action message</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>8607</td>
<td>Recovery boot has been initiated. Note: The Primary BIOS image may be corrupted or the system may hang during POST. A BIOS update is required.</td>
<td></td>
<td>Fatal</td>
</tr>
<tr>
<td>A100</td>
<td>BIOS ACM Error</td>
<td></td>
<td>Major</td>
</tr>
<tr>
<td>A421</td>
<td>PCI component encountered a SERR error</td>
<td></td>
<td>Fatal</td>
</tr>
<tr>
<td>A5A0</td>
<td>PCI Express component encountered a PERR error</td>
<td></td>
<td>Minor</td>
</tr>
<tr>
<td>A5A1</td>
<td>PCI Express component encountered an SERR error</td>
<td></td>
<td>Fatal</td>
</tr>
<tr>
<td>A6A0</td>
<td>DXE Boot Services driver: Not enough memory available to shadow a Legacy Option ROM.</td>
<td>Disable OpRom at SETUP to save runtime memory.</td>
<td>Minor</td>
</tr>
</tbody>
</table>

### F.1 POST Error Beep Codes

The following table lists the POST error beep codes. Before system video initialization, the BIOS uses these beep codes to inform users on error conditions. The beep code is followed by a user-visible code on the POST progress LEDs.

**Table 13. POST Error Beep Codes**

<table>
<thead>
<tr>
<th>Beeps</th>
<th>Error Message</th>
<th>POST Progress Code</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 short</td>
<td>USB device action</td>
<td>N/A</td>
<td>Short beep sounded whenever USB device is discovered in POST or inserted or removed during runtime.</td>
</tr>
<tr>
<td>3 short</td>
<td>Memory error</td>
<td>Multiple</td>
<td>System halted because a fatal error related to the memory was detected.</td>
</tr>
<tr>
<td>3 long and 1 short</td>
<td>CPU mismatch error</td>
<td>E5, E6</td>
<td>System halted because a fatal error related to the CPU family/core/cache mismatch was detected.</td>
</tr>
</tbody>
</table>

The integrated BMC may generate beep codes upon detection of failure conditions. Beep codes are sounded each time the problem is discovered, such as on each power-up attempt, but are not sounded continuously. Codes that are common across all Intel server boards and systems that use same generation chipset are listed in the following table. Each digit in the code is represented by a sequence of beeps whose count is equal to the digit.

**Table 14. Integrated BMC Beep Codes**

<table>
<thead>
<tr>
<th>Code</th>
<th>Reason for Beep</th>
<th>Associated Sensors</th>
</tr>
</thead>
<tbody>
<tr>
<td>1-5-2-1</td>
<td>No CPUs installed or first CPU socket is empty</td>
<td>CPU Missing Sensor</td>
</tr>
<tr>
<td>1-5-2-4</td>
<td>MSID mismatch occurs if a processor is installed into a system board that has incompatible power capabilities.</td>
<td>MSID Mismatch Sensor</td>
</tr>
<tr>
<td>1-5-4-2</td>
<td>DC power unexpectedly lost (power good dropout) – Power unit sensors report power unit failure offset.</td>
<td>Power fault</td>
</tr>
<tr>
<td>1-5-4-4</td>
<td>Power control fault (power good assertion timeout).</td>
<td>Power unit – soft power control failure offset</td>
</tr>
<tr>
<td>1-5-1-2</td>
<td>VR Watchdog Timer sensor assertion</td>
<td>VR Watchdog Timer</td>
</tr>
<tr>
<td>1-5-1-4</td>
<td>The system does not power on or unexpectedly power off and a power supply unit (PSU) is present that is an incompatible model with one or more other PSUs in the system</td>
<td>PS Status</td>
</tr>
</tbody>
</table>
F.2 Processor Initialization Error Summary

The following table describes mixed processor conditions and actions for all Intel server boards and Intel server systems designed with the Intel Xeon Scalable processor family architecture. The errors fall into one of the following categories:

- **Fatal**: If the system cannot boot, POST halts and delivers the following error message to the BIOS Setup Error Manager screen:

  Unrecoverable fatal error found. System will not boot until the error is resolved
  Press <F2> to enter setup

  When the <F2> key is pressed, the error message is displayed on the BIOS Setup Error Manager screen and an error is logged to the system event log (SEL) with the POST error code.

  The “POST Error Pause” option setting in the BIOS Setup does not affect this error.

  If the system is not able to boot, the system generates a beep code consisting of three long beeps and one short beep. The system cannot boot unless the error is resolved. The faulty component must be replaced.

  The system status LED is set to a steady amber color for all fatal errors that are detected during processor initialization. A steady amber system status LED indicates that an unrecoverable system failure condition has occurred.

- **Major**: An error message is displayed to the Error Manager screen and an error is logged to the SEL. If the BIOS Setup option “Post Error Pause” is enabled, operator intervention is required to continue booting the system. If the BIOS Setup option “POST Error Pause” is disabled, the system continues to boot.

- **Minor**: An error message may be displayed to the screen or to the BIOS Setup Error Manager and the POST error code is logged to the SEL. The system continues booting in a degraded state. The user may want to replace the erroneous unit. The “POST Error Pause” option setting in the BIOS Setup does not affect this error.

<table>
<thead>
<tr>
<th>Error</th>
<th>Severity</th>
<th>System Action when BIOS Detects the Error Condition</th>
</tr>
</thead>
</table>
| Processor family not identical           | Fatal    | - Halts at POST code 0xE6.  
- Halts with three long beeps and one short beep.  
- Takes fatal error action (see above) and does not boot until the fault condition is remedied. |
| Processor model not identical             | Fatal    | - Logs the POST error code into the SEL.  
- Alerts the BMC to set the system status LED to steady amber. 
- Displays 0196: Processor model mismatch detected message in the error manager.  
- Takes fatal error action (see above) and does not boot until the fault condition is remedied. |
| Processor cores/threads not identical     | Fatal    | - Halts at POST code 0xE5.  
- Halts with three long beeps and one short beep.  
- Takes fatal error action (see above) and does not boot until the fault condition is remedied. |
| Processor cache or home agent not identical | Fatal  | - Halts at POST code 0xE5.  
- Halts with three long beeps and one short beep.  
- Takes fatal error action (see above) and does not boot until the fault condition is remedied. |
<table>
<thead>
<tr>
<th>Error</th>
<th>Severity</th>
<th>System Action when BIOS Detects the Error Condition</th>
</tr>
</thead>
<tbody>
<tr>
<td>Processor frequency (speed)</td>
<td>Fatal</td>
<td>If the frequencies for all processors can be adjusted to be the same:</td>
</tr>
<tr>
<td>not identical</td>
<td></td>
<td>• Adjusts all processor frequencies to the highest common frequency.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Does not generate an error – this is not an error condition.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Continues to boot the system successfully.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>If the frequencies for all processors cannot be adjusted to be the same:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Logs the POST error code into the SEL.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Alerts the BMC to set the system status LED to steady amber.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Does not disable the processor.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Displays 0197: Processor speeds unable to synchronize message in the error manager.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Takes fatal error action (see above) and does not boot until the fault condition is remedied.</td>
</tr>
<tr>
<td>Processor Intel® UPI link</td>
<td>Fatal</td>
<td>If the link frequencies for all Intel® Ultra Path Interconnect (Intel® UPI) links can be adjusted to be the same:</td>
</tr>
<tr>
<td>link frequencies not identical</td>
<td></td>
<td>• Adjusts all Intel® UPI interconnect link frequencies to highest common frequency.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Does not generate an error – this is not an error condition.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Continues to boot the system successfully.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>If the link frequencies for all Intel® UPI links cannot be adjusted to be the same:</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Logs the POST error code into the SEL.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Alerts the BMC to set the system status LED to steady amber.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Does not disable the processor.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Displays 0195: Processor Intel® UPII link frequencies unable to synchronize message in the error manager.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Takes fatal error action (see above) and does not boot until the fault condition is remedied.</td>
</tr>
<tr>
<td>Processor microcode update</td>
<td>Major</td>
<td>• Logs the POST error code into the SEL.</td>
</tr>
<tr>
<td>update failed</td>
<td></td>
<td>• Displays 816x: Processor 0x unable to apply microcode update message in the error manager or on the screen.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• Takes major error action. The system may continue to boot in a degraded state, depending on the “POST Error Pause” setting in setup, or may halt with the POST error code in the error manager waiting for operator intervention.</td>
</tr>
<tr>
<td>Processor microcode update</td>
<td>Minor</td>
<td>• Logs the POST error code into the SEL.</td>
</tr>
<tr>
<td>update missing</td>
<td></td>
<td>• Displays 818x: Processor 0x microcode update not found message in the error manager or on the screen.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>• The system continues to boot in a degraded state, regardless of the “POST Error Pause” setting in setup.</td>
</tr>
</tbody>
</table>
Appendix G. System Packaging Assembly Instructions

The original Intel packaging, in which the server system is delivered, is designed to provide protection to a fully configured system and was tested to meet ISTA (International Safe Transit Association) Test Procedure 3A (2008). The packaging was also designed to be re-used for shipment after system integration has been completed.

The original packaging includes two layers of boxes – an inner box and the outer shipping box, and various protective inner packaging components. The boxes and packaging components are designed to function together as a protective packaging system. When reused, all of the original packaging material must be used, including both boxes and each inner packaging component. In addition, all inner packaging components MUST be reinstalled in the proper location to ensure adequate protection of the system for subsequent shipment.

**Note:** The design of the inner packaging components does not prevent improper placement within the packaging assembly. There is only one correct packaging assembly that will allow the package to meet the ISTA (International Safe Transit Association) Test Procedure 3A (2008) limits.

Failure to follow the specified packaging assembly instructions may result in damage to the system during shipment.

1. Open the outer box (iPN K65051-001). Fold the flap slightly to avoid cracks on the corners. Place the taped inner box (iPN K65084-001) into the outer box. See the following figure.

**Caution:** No cracks on the corners are permitted.
2. Place the front cushion (iPN K65031-001) on the left end of the inner box. Place the rear cushion (K65006-001) on the right end of the inner box. Then, place bottom foam K75566-001 in the middle. See the following figure.

3. Pack the server system with red EPE sheet (iPN K58342-001). See the following figure.

4. Pack the system wrapped with EPE sheet in the EPE bag. Then, place the system on the bottom foam (see the following figure). Place the top front foam (iPN K65046-001) on the left and the top rear foam (iPN K65037-001) on the right.
5. Place the accessory kit box (iPN H49469-001) in the center foam cushion. Place the extension rails in the two narrow foam cavities. See the following figure.

6. Close the inner box.

7. Close the outer box. Seal the entire package with tape as shown in the following figure.
WARNING: English (US)

The power supply in this product contains no user-serviceable parts. There may be more than one supply in this product. Refer servicing only to qualified personnel.

Do not attempt to modify or use the supplied AC power cord if it is not the exact type required. A product with more than one power supply will have a separate AC power cord for each supply.

The power button on the system does not turn off system AC power. To remove AC power from the system, you must unplug each AC power cord from the wall outlet or power supply.

The power cord(s) is considered the disconnect device to the main (AC) power. The socket outlet that the system plugs into shall be installed near the equipment and shall be easily accessible.

SAFETY STEPS: Whenever you remove the chassis covers to access the inside of the system, follow these steps:

1. Turn off all peripheral devices connected to the system.
2. Turn off the system by pressing the power button.
3. Unplug all AC power cords from the system or from wall outlets.
4. Label and disconnect all cables connected to I/O connectors or ports on the back of the system.
5. Provide some electrostatic discharge (ESD) protection by wearing an antistatic wrist strap attached to chassis ground of the system—any unpainted metal surface—when handling components.
6. Do not operate the system with the chassis covers removed.

After you have completed the six SAFETY steps above, you can remove the system top covers. To do this:

1. Unlock and remove the padlock from the back of the system if a padlock has been installed.
2. Remove and save all screws from the covers.
3. Remove the covers.
For proper cooling and airflow, always reinstall the chassis covers before turning on the system. Operating the system without the covers in place can damage system parts. To install the covers:

1. Check first to make sure you have not left loose tools or parts inside the system.
2. Check that cables, add-in boards, and other components are properly installed.
3. Attach the covers to the chassis with the screws removed earlier, and tighten them firmly.
4. Insert and lock the padlock to the system to prevent unauthorized access inside the system.
5. Connect all external cables and the AC power cord(s) to the system.

A microprocessor and heat sink may be hot if the system has been running. Also, there may be sharp pins and edges on some board and chassis parts. Contact should be made with care. Consider wearing protective gloves.

Danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the equipment manufacturer. Dispose of used batteries according to manufacturer’s instructions.

The system is designed to operate in a typical office environment. Choose a site that is:

- Clean and free of airborne particles (other than normal room dust).
- Well ventilated and away from sources of heat including direct sunlight.
- Away from sources of vibration or physical shock.
- Isolated from strong electromagnetic fields produced by electrical devices.
- In regions that are susceptible to electrical storms, we recommend you plug your system into a surge suppresser and disconnect telecommunication lines to your modem during an electrical storm.
- Provided with a properly grounded wall outlet.
- Provided with sufficient space to access the power supply cord(s), because they serve as the product’s main power disconnect.
ОСТОРОЖНО: русский

Блок питания данного изделия не содержит деталей, подлежащих обслуживанию пользователем. В этом изделии может быть несколько блоков питания. Обслуживание должно выполняться только квалифицированным персоналом.

Не модифицируйте и не используйте прилагаемый кабель питания, если он не соответствует требуемому типу. Если в устройстве несколько блоков питания, то к каждому блоку питания прилагается отдельный кабель питания.

При нажатии кнопки питания не отключается питание системы от электросети. Чтобы отключить подачу питания переменного тока в систему, необходимо отсоединить все кабели питания от электрической розетки или блока питания.

Кабель питания считается размыкателем питания переменного тока. Электрическая розетка, к которой подключается система, должна находиться рядом с оборудованием и быть легко доступной.

ИНСТРУКЦИИ ПО ТЕХНИКЕ БЕЗОПАСНОСТИ. Каждый раз перед снятием крышек корпуса для доступа к внутренней части системы выполняйте следующие действия:

1. Выключите все периферийные устройства, подключенные к системе.
2. Выключите систему, нажав кнопку питания.
3. Отсоедините все кабели питания от системы или электрических розеток.
4. Промаркируйте и отсоедините все кабели, подключенные к разъемам или портам ввода/вывода на задней панели системы.
5. Для обеспечения защиты от электростатического разряда при работе с компонентами надевайте антистатический браслет, прикрепленный к заземленной части корпуса системы (любой неокрашенной металлической поверхности).
6. Запрещается работать с системой, когда крышки корпуса сняты.

Крышки корпуса системы можно снимать, когда выполнены все шесть описанных выше мер безопасности. Для этого:

1. Откройте и снимите навесной замок (если имеется) с задней части системы.
2. Выверните все винты с крышек и сохраните их.
3. Снимите крышки.

(продолжение)
ОСТОРОЖНО: русский (продолжение)

Для обеспечения надлежащего охлаждения и воздушного потока всегда устанавливайте на место крышки корпуса перед включением системы. Работа системы без установленных крышек может привести к повреждению компонентов системы. Чтобы установить крышки, выполните следующие действия:

1. Сначала проверьте, не осталось ли в системе незакрепленных инструментов или деталей.
2. Убедитесь, что кабели, платы расширения и другие компоненты установлены правильно.
3. Закрепите крышки на корпусе, завернув и надежно затянуть винты, снятые ранее.
4. Установите и закройте навесной замок для предотвращения несанкционированного доступа внутрь системы.
5. Подключите к системе все внешние кабели и кабели питания.

Система предназначена для работы в обычной офисной среде. Место установки системы должно соответствовать следующим требованиям:

- Помещение должно быть чистым, в воздухе не должно быть взвешенных частиц (кроме обычной пыли).
- Место установки должно хорошо вентилироваться и находиться вдали от источников тепла (включая прямой солнечный свет).
- Место установки должно находиться вдали от источников вибрации или механических ударов.
- Место установки должно быть изолировано от сильных электромагнитных полей, создаваемых электрическими устройствами.
- В регионах, где часто бывает гроза, рекомендуется подключать систему к сетевому фильтру и отключать телекоммуникационные линии от модема во время грозы.
- В помещении должна быть правильно заземленная электрическая розетка.
- Должен быть оставлен достаточный зазор для доступа к кабелям питания, которые служат размыкателем электропитания системы.
УВАГА! Українська

Джерело живлення в цьому виробі не містить жодних частин, які користувачі могли б обслуговувати самостійно. Цей виріб може містити більше одного джерела живлення. Обслуговувати його може виключно кваліфікований персонал.

Не намагайтеся модифікувати шнур живлення змінного струму з комплекту або користуватися ним, якщо він не відповідає потрібному типу. Виріб із джерелами живлення більше одного має окремі шнури живлення змінного струму для кожного джерела.

Кнопка живлення на системі не вимикає живлення змінного струму системи. Щоб позбавити систему змінного струму, слід вийняти всі шнури живлення змінного струму зі стінних розеток або джерел живлення. Вважається, що шнур(и) живлення є пристроями вимкнення основного живлення (змінного струму). Розетка електромережі, до якої підключається система, мусить бути розташована поруч із обладнанням і легко доступна.

КРОКИ БЕЗПЕКИ: Щоразу, знімаючи корпус для доступу до внутрішніх частин системи, виконуйте ці кроки:

7. Вимкніть усі периферійні пристрої, підключені до системи.
8. Вимкніть систему, натиснувши кнопку живлення.
9. Вийміть шнури живлення змінного струму із системи чи стінних розеток.
10. Позначте і від’єднайте всі кабелі, підключені до з’єднувачів входу/виходу або портів ззаду на системі.
11. Працюючи з компонентами, захищаючись від електростатичних розрядів (ЕР), вдягаючи антистатичний ремінець-браслет, прикріплений до елемента заземлення корпусу - будь-якої непофарбованої металевої поверхні.
12. Не використовуйте систему з відкритим корпусом.

Після виконання шести наведених вище кроків БЕЗПЕКИ можна знімати корпус (кришки) з системи. Для цього виконайте такі дії:

4. Розблокуйте і зніміть замок ззаду на системі, якщо його встановлено.
5. Зніміть і зберігайте всі гвинти з кришок.
6. Зніміть усі кришки.

продовження
УВАГА! Українська (продовження)

Для правильної охолодження та вентиляції завжди повертайте на місце кришки корпусу перед увімкненням системи. Робота системи без кришок може пошкодити деталі системи. Щоб установити кришки, виконайте такі дії:

6. Спочатку переконайтеся, що всередині системи не залишилося деталей або незакріплених інструментів.
7. Перевірте, чи правильно встановлено кабелі, розширувальні плати та інші компоненти.
8. Прикріпіть кришки до корпусу знятими раніше гвинтами та надійно їх затягніть.
9. Вставте в систему і зафіксуйте замок, щоб запобігти неавторизованому доступу до нього.
10. Підключіть усі зовнішні кабелі та шнур(и) живлення змінного струму до системи.

Під час роботи системи мікропроцесор і радіатор можуть розігрітися до гарячого. Деякі частини корпусу і плат можуть мати гострі шипи або краї. Із ними слід поводитися обережно. Можна вдягти захисні рукавички.

Загроза вибуху, якщо батарею замінено на неправильну. Замінюють лише таким самим або еквівалентним типом, рекоменсованим виробником. Утилізуйте використані батареї згідно з інструкціями виробника.

Систему створено для роботи в типовому офісному приміщенні. Виберіть місце, яке:

- Чисте і де немає в повітрі інших дрібних часточок, окрім звичайного побутового пилу.
- Добре провітрується, розташовано далеко від джерел тепла, включно з прямим сонячним промінням.
- Розташовано далеко від джерел вібрацій і струсяв.
- Ізольоване від сильних електромагнітних полів, спричинених електроприладами.
- У регіонах, де часто проходять грози, радимо підключати пристрій через пристрій захисту від викидів напруги та відключити телекомунікаційні лінії від модему під час грози.
- Оснащене правильно заземленими стінними розетками електромережі.
- Має достатньо простору для доступу до шнура(ів) живлення, оскільки вони слугують основними вимикачами виробу.
AVERTISSEMENT: Français

Le bloc d'alimentation de ce produit ne contient aucune pièce pouvant être réparée par l'utilisateur. Ce produit peut contenir plus d'un bloc d'alimentation. Veuillez contacter un technicien qualifié en cas de problème.

Ne pas essayer d'utiliser ni modifier le câble d'alimentation CA fourni, s'il ne correspond pas exactement au type requis. Le nombre de câbles d'alimentation CA fournis correspond au nombre de blocs d'alimentation du produit.

Notez que le commutateur CC de mise sous tension/hors tension du panneau avant n'éteint pas l'alimentation CA du système. Pour mettre le système hors tension, vous devez débrancher chaque câble d'alimentation de sa prise.

CONSIGNES DE SÉCURITÉ: Lorsque vous ouvrez le boîtier pour accéder à l'intérieur du système, suivez les consignes suivantes:

1. Mettez hors tension tous les périphériques connectés au système.
2. Mettez le système hors tension en mettant l'interrupteur général en position OFF (bouton-poussoir).
3. Débranchez tous les cordons d'alimentation c.a. du système et des prises murales.
4. Identifiez et débranchez tous les câbles reliés aux connecteurs d'E-S ou aux accès derrière le système.
5. Pour prévenir les décharges électrostatiques lorsque vous touchez aux composants, portez une bande antistatique pour poignet et reliez-la à la masse du système (toute surface métallique non peinte du boîtier).
6. Ne faites pas fonctionner le système tandis que le boîtier est ouvert.

Une fois TOUTES les étapes précédentes accomplies, vous pouvez retirer les panneaux du système. Procédez comme suit:

1. Si un cadenas a été installé sur à l’arrière du système, déverrouillez-le et retirez-le.
2. Retirez toutes les vis des panneaux et mettez-les dans un endroit sûr.
3. Retirez les panneaux.
Afin de permettre le refroidissement et l'aération du système, réinstallez toujours les panneaux du boîtier avant de mettre le système sous tension. Le fonctionnement du système en l'absence des panneaux risque d'endommager ses pièces. Pour installer les panneaux, procédez comme suit:

1. Assurez-vous de ne pas avoir oublié d'outils ou de pièces démontées dans le système.
2. Assurez-vous que les câbles, les cartes d'extension et les autres composants sont bien installés.
3. Revissez solidement les panneaux du boîtier avec les vis retirées plus tôt.
4. Remettez le cadenas en place et verrouillez-le afin de prévenir tout accès non autorisé à l'intérieur du système.
5. Rebranchez tous les cordons d'alimentation c. a. et câbles externes au système.

Le microprocesseur et le dissipateur de chaleur peuvent être chauds si le système a été sous tension. Faites également attention aux broches aiguës des cartes et aux bords tranchants du capot. Nous vous recommandons l'usage de gants de protection.

Danger d'explosion si la batterie n'est pas remontée correctement. Remplacer uniquement avec une batterie du même type ou d'un type équivalent recommandé par le fabricant. Disposez des piles usées selon les instructions du fabricant.

Le système a été conçu pour fonctionner dans un cadre de travail normal. L'emplacement choisi doit être:

- Propre et dépourvu de poussière en suspension (sauf la poussière normale).
- Bien aéré et loin des sources de chaleur, y compris du soleil direct.
- A l'abri des chocs et des sources de vibrations.
- Isolé de forts champs électromagnétiques générés par des appareils électriques.
- Dans les régions sujettes aux orages magnétiques il est recommandé de brancher votre système à un suppresseur de surtension, et de débrancher toutes les lignes de télécommunications de votre modem durant un orage.
- Muni d'une prise murale correctement mise à la terre.
- Suffisamment spacieux pour vous permettre d'accéder aux câbles d'alimentation (ceux-ci étant le seul moyen de mettre le système hors tension).
**WARNUNG: Deutsch**


Versuchen Sie nicht, das mitgelieferte Netzkabel zu ändern oder zu verwenden, wenn es sich nicht genau um den erforderlichen Typ handelt. Ein Produkt mit mehreren Netzgeräten hat für jedes Netzgerät ein eigenes Netzkabel.


**SICHERHEISMASSNAHMEN:** Immer wenn Sie die Gehäuseabdeckung abnehmen um an das Systeminnere zu gelangen, sollten Sie folgende Schritte beachten:

1. Schalten Sie alle an Ihr System angeschlossenen Peripheriegeräte aus.
2. Schalten Sie das System mit dem Hauptschalter aus.
5. Tragen Sie ein geerdetes Antistatik Gelenkband, um elektrostatische Ladungen (ESD) über blanke Metallstellen bei der Handhabung der Komponenten zu vermeiden.

Nachdem Sie die oben erwähnten ersten sechs SICHERHEITSSCHRITTE durchgeführt haben, können Sie die Abdeckung abnehmen, indem Sie:

1. Öffnen und entfernen Sie die Verschlußeinrichtung (Padlock) auf der Rückseite des Systems, falls eine Verschlußeinrichtung installiert ist.
2. Entfernen Sie alle Schrauben der Gehäuseabdeckung.
3. Nehmen Sie die Abdeckung ab.
Zur ordnungsgemäßen Kühlung und Lüftung muß die Gehäuseabdeckung immer wieder vor dem Einschalten installiert werden. Ein Betrieb des Systems ohne angebrachte Abdeckung kann Ihrem System oder Teile darin beschädigen. Um die Abdeckung wieder anzubringen:

1. Vergewissern Sie sich, daß Sie keine Werkzeuge oder Teile im Innern des Systems zurückgelassen haben.
2. Überprüfen Sie alle Kabel, Zusatzkarten und andere Komponenten auf ordnungsgemäßen Sitz und Installation.
3. Bringen Sie die Abdeckungen wieder am Gehäuse an, indem Sie die zuvor gelösten Schrauben wieder anbringen. Ziehen Sie diese gut an.
4. Bringen Sie die Verschlußeinrichtung (Padlock) wieder an und schließen Sie diese, um ein unerlaubtes Öffnen des Systems zu verhindern.


Das System wurde für den Betrieb in einer normalen Büroumgebung entwickelt. Der Standort sollte:

- sauber und staubfrei sein (Hausstaub ausgenommen);
- gut gelüftet und keinen Heizquellen ausgesetzt sein (einschließlich direkter Sonneneinstrahlung);
- keinen Erschütterungen ausgesetzt sein;
- keine starken, von elektrischen Geräten erzeugten elektromagnetischen Felder aufweisen;
- in Regionen, in denen elektrische Stürme auftreten, mit einem Überspannungsschutzgerät verbunden sein; während eines elektrischen Sturms sollte keine Verbindung der Telekommunikationsleitungen mit dem Modem bestehen;
- mit einer geerdeten Wechselstromsteckdose ausgerüstet sein;
- über ausreichend Platz verfügen, um Zugang zu den Netzkabeln zu gewährleisten, da der Stromanschluß des Produkts hauptsächlich über die Kabel unterbrochen wird.
AVVERTENZA: Italiano

Rivolgersi ad un tecnico specializzato per la riparazione dei componenti dell'alimentazione di questo prodotto. È possibile che il prodotto disponga di più fonti di alimentazione.

Non modificare o utilizzare il cavo di alimentazione in c.a. fornito dal produttore, se non corrisponde esattamente al tipo richiesto. Ad ogni fonte di alimentazione corrisponde un cavo di alimentazione in c.a. separato.

L'interruttore attivato/disattivato nel pannello anteriore non interrompe l'alimentazione in c.a. del sistema. Per interromperla, è necessario scollegare tutti i cavi di alimentazione in c.a. dalle prese a muro o dall'alimentazione di corrente.

PASSI DI SICUREZZA: Qualora si rimuovano le coperture del telaio per accedere all'interno del sistema, seguire i seguenti passi:
1. Spegnere tutti i dispositivi periferici collegati al sistema.
2. Spegnere il sistema, usando il pulsante spento/acceso dell'interruttore del sistema.
3. Togliere tutte le spine dei cavi del sistema dalle prese elettriche.
4. Identificare e sconnettere tutti i cavi attaccati ai collegamenti I/O od alle prese installate sul retro del sistema.
5. Qualora si tocchino i componenti, proteggersi dallo scarico elettrostatico (SES), portando un cinghia anti-statica da polso che è attaccata alla presa a terra del telaio del sistema – qualsiasi superficie non dipinta –.
6. Non far operare il sistema quando il telaio è senza le coperture.

Dopo aver seguito i sei passi di SICUREZZA sopracitati, togliere le coperture del telaio del sistema come seque:
1. Aprire e rimuovere il lucchetto dal retro del sistema qualora ve ne fosse uno installato.
2. Togliere e mettere in un posto sicuro tutte le viti delle coperture.
3. Togliere le coperture.
Per il giusto flusso dell'aria e raffreddamento del sistema, rimettere sempre le coperture del telaio prima di riaccendere il sistema. Operare il sistema senza le coperture al loro proprio posto potrebbe danneggiare i componenti del sistema. Per rimettere le coperture del telaio:

1. Controllare prima che non si siano lasciati degli attrezzi o dei componenti dentro il sistema.
2. Controllare che i cavi, dei supporti aggiuntivi ed altri componenti siano stati installati appropriatamente.
3. Attaccare le coperture al telaio con le viti tolte in precedenza e avvitarle strettamente.
4. Inserire e chiudere a chiave il lucchetto sul retro del sistema per impedire l'accesso non autorizzato al sistema.
5. Ricollegare tutti i cavi esterni e le prolunghe AC del sistema.

Se il sistema è stato a lungo in funzione, il microprocessore e il dissipatore di calore potrebbero essere surriscaldati. Fare attenzione alla presenza di piedini appuntiti e parti taglienti sulle schede e sul telaio. È consigliabile l'uso di guanti di protezione.

Esiste il pericolo di un esplosione se la pila non viene sostituita in modo corretto. Utilizzare solo pile uguali o di tipo equivalente a quelle consigliate dal produttore. Per disfarsi delle pile usate, seguire le istruzioni del produttore.

Il sistema è progettato per funzionare in un ambiente di lavoro tipo. Scegliere una postazione che sia:

- Pulita e libera da particelle in sospensione (a parte la normale polvere presente nell'ambiente).
- Ben ventilata e lontana da fonti di calore, compresa la luce solare diretta.
- Al riparo da urti e lontana da fonti di vibrazione.
- Isolata dai forti campi magnetici prodotti da dispositivi elettrici.
- In aree soggette a temporali, è consigliabile collegare il sistema ad un limitatore di corrente. In caso di temporali, scollegare le linee di comunicazione dal modem.
- Dotata di una presa a muro correttamente installata.
- Dotata di spazio sufficiente ad accedere ai cavi di alimentazione, i quali rappresentano il mezzo principale di scollegamento del sistema.
ADVERTENCIAS: Español

El usuario debe abstenerse de manipular los componentes de la fuente de alimentación de este producto, cuya reparación debe dejarse exclusivamente en manos de personal técnico especializado. Puede que este producto disponga de más de una fuente de alimentación.

No intente modificar ni usar el cable de alimentación de corriente alterna, si no corresponde exactamente con el tipo requerido.

El número de cables suministrados se corresponden con el número de fuentes de alimentación de corriente alterna que tenga el producto.

Nótese que el interruptor activado/desactivado en el panel frontal no desconecta la corriente alterna del sistema. Para desconectarla, deberá desenchufar todos los cables de corriente alterna de la pared o desconectar la fuente de alimentación.

INSTRUCCIONES DE SEGURIDAD: Cuando extraiga la tapa del chasis para acceder al interior del sistema, siga las siguientes instrucciones:

1. Apague todos los dispositivos periféricos conectados al sistema.
2. Apague el sistema presionando el interruptor encendido/apagado.
3. Desconecte todos los cables de alimentación CA del sistema o de las tomas de corriente alterna.
4. Identifique y desconecte todos los cables enchufados a los conectores E/S o a los puertos situados en la parte posterior del sistema.
5. Cuando manipule los componentes, es importante protegerse contra la descarga electrostática (ESD). Puede hacerlo si utiliza una muñequera antiestática sujetada a la toma de tierra del chasis — o a cualquier tipo de superficie de metal sin pintar.
6. No ponga en marcha el sistema si se han extraído las tapas del chasis.

Después de completar las seis instrucciones de SEGURIDAD mencionadas, ya puede extraer las tapas del sistema. Para ello:

1. Desbloquee y extraiga el bloqueo de seguridad de la parte posterior del sistema, si se ha instalado uno.
2. Extraiga y guarde todos los tornillos de las tapas.
3. Extraiga las tapas.
Para obtener un enfriamiento y un flujo de aire adecuados, reinstale siempre las tapas del chasis antes de poner en marcha el sistema. Si pone en funcionamiento el sistema sin las tapas bien colocadas puede dañar los componentes del sistema. Para instalar las tapas:

1. Asegúrese primero de no haber dejado herramientas o componentes sueltos dentro del sistema.
2. Compruebe que los cables, las placas adicionales y otros componentes se hayan instalado correctamente.
3. Incorpore las tapas al chasis mediante los tornillos extraídos anteriormente, tensándolos firmemente.
4. Inserte el bloqueo de seguridad en el sistema y bloquéelo para impedir que pueda accederse al mismo sin autorización.
5. Conecte todos los cables externos y los cables de alimentación CA al sistema.

Si el sistema ha estado en funcionamiento, el microprocesador y el disipador de calor pueden estar aún calientes. También conviene tener en cuenta que en el chasis o en el tablero puede haber piezas cortantes o punzantes. Por ello, se recomienda precaución y el uso de guantes protectores.

Existe peligro de explosión si la pila no se cambia de forma adecuada. Utilice solamente pilas iguales o del mismo tipo que las recomendadas por el fabricante del equipo. Para deshacerse de las pilas usadas, siga igualmente las instrucciones del fabricante.

El sistema está diseñado para funcionar en un entorno de trabajo normal. Escoja un lugar:

- Limpio y libre de partículas en suspensión (salvo el polvo normal).
- Bien ventilado y alejado de fuentes de calor, incluida la luz solar directa.
- Alejado de fuentes de vibración.
- Aislado de campos electromagnéticos fuertes producidos por dispositivos eléctricos.
- En regiones con frecuentes tormentas eléctricas, se recomienda conectar su sistema a un eliminador de sobrevoltaje y desconectar el módem de las líneas de telecomunicación durante las tormentas.
- Provisto de una toma de tierra correctamente instalada.
- Provisto de espacio suficiente como para acceder a los cables de alimentación, ya que éstos hacen de medio principal de desconexión del sistema.
אזהרה: עברית

אספקת החשמל başında או מיכילה חלקי ההרכבה על ידי מضعفות. ייתכן שיש יותר ממתקי אספקת חשמל.

אוחזר: עברית

אספקת החשמל מבוצרת או מיכילה חלקי ההרכבה על ידי מضعفות. ייתכן שיש יותר ממתקי אספקת חשמל.

1. לא י꼴ו שלושת עמדות החשמל א"י; המפסק. א"י;משקל, אם הותקן במודול נפרד. למרכזה ע"י ממקורי.

2. אספקת השמאל המוקר א"י; מעל שלושה מפרים לכל ממקור אספקת השמאל.

3. מטות להפסת המפסק והמסגרת שלושה "א"י"; משקל, אם הותקן במודול נפרד. למרכזה ע"י ממקורי.

4. הבנני או "א"י"; משקעים (להמקורי) והמסגרת שלושה "א"י"; משקעים שלושה ממקורים אספקת השמאל במקורים ע"י ממקורי.

5. ליציזו וויתו גב"ל במקורים ע"י ממקורי.

שלבים בטיחות: בכל פעמיות ממתקי א"י, ממתקי המפסקים נדרשים להסרת חלקי הפסידה של המפסקים. יש לبرش את העצמים הבאות:

1. לכבוש את כל מהופות המפסקים ממתקי המפסקים.

2. לכבוש את המפסקים על ידי הפסידת על פ与时 העבודה.

3. לאור את כל מבילי המפסקים ממתקי המפסקים, אם הותקן במודול נפרד. למרכזה ע"י ממקורי.

4. ל㎏ו את כל מבילי המפסקים ממתקי המפסקים, אם הותקן במודול נפרד. למרכזה ע"י ממקורי.

5. אם מתקי עסקה מפורת ספר KathrynSituate או מרחבי כר (ESD) על ידי ה other קיימת שרשך כדי אנטי-סטטיית ש rencontיה. 

6. לאור את המפסקים ממתקי המפסקים ממתקי המפסקים, אם הותקן במודול נפרד. למרכזה ע"י ממקורי.

לאחר השלמת השלב שלבים הבאים לילך, ב鞍שם ממתקי המפסקים ממתקי המפסקים, יין לעשת את:

1. לק Roose את המפסקים ממתקי המפסקים ממתקי המפסקים, אם הותקן במודול נפרד. למרכזה ע"י ממקורי.

2. לק Roose את המפסקים ממתקי המפסקים ממתקי המפסקים, אם הותקן במודול נפרד. למרכזה ע"י ממקורי.

3. לק Roose את המפסקים ממתקי המפסקיםummMK民。
לקירור ולזרימת אוויר תקינים, יש תמיד להתקין מחדש את מכסי המעטפת לפני הפעלת המערכת. הפעלת המערכת ללא המכסים במקומם, עלולה לגרום לחד והתק藓 בעור במערכת.

1. יש לבחוק החלון כי זה לא奭א נSEAר כים או חלקי רופפים בחודו.
2. יש לבחוק שהواشنطن, הלוחות הנוספים והיתר חומרים מחוברים правильно.
3. יש לבחוק את המכוסים ולפשוש על הברגים של הלוחות על חוף החלון.
4. יש Lópezים את מנעלי החלון לערוך ולעשות ירי לצלוגע ויגיע לבלי מרת手続き לעודף הקספראן.
5. יש לבחוק את כל מכוסי החוזנותיות ואת הבול (2) של למעפטה.

מע用品 תקינה. יש לנטה את למעפתה. יש לשקוף עדיה כסופה ענה.

סנקת פיזור ואNotEmpty סוללה מתון במהלך זמן לאחר שבירה. יש להיתקל בק סוללה או חישוב וрактиות על ידי יצרן חומר. יש

לחלק סוללה עם פשיטה על פולר הור搌ר.

המערכת מת недоיה לפונל בפסיב מ的带领 תופעים. יש לבחור את שנה:

• נק הופיסה מתהליך גישה במלאי (לא العمבק ש끼יה עם רגיל הבד).<
• יאןור רוחק ומקורה בחל על לחוב הים. יש להיתקל מתהליך רמות כדי לה打得 פליז.
• יש להיתקל את תהליך רמות بينما לה打得 פליז.
• מבחר מתהליךLATED גישה מתון (בר לא חירה שישה על ידי מובילית שמילון.
• מבחר שגריר ENUM להדרה מתון, חיבר ממליצים לפני מתהליך מתון, יՁר חזר המשנה לוח קור.
• חיבר ממליצים לפני מתהליך מתון, יヅר חזר המשנה לוח קור.
• הצלוגע קור מאורית הרב.
• בוט מפסיר ממקה לתלשה והופישה לבלב(2) החולמות, מוכנים נוזח/יתו/ת והMeasured(ו) לקלים רית חיים למטרה.
## Appendix I. Glossary

<table>
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<tr>
<th>Term</th>
<th>Definition</th>
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<tr>
<td>ACPI</td>
<td>Advanced Configuration and Power Interface</td>
</tr>
<tr>
<td>ASHRAE</td>
<td>American Society of Heating, Refrigerating and Air-Conditioning Engineers</td>
</tr>
<tr>
<td>BBS</td>
<td>BIOS Boot Specification</td>
</tr>
<tr>
<td>BMC</td>
<td>Baseboard Management Controller</td>
</tr>
<tr>
<td>BIOS</td>
<td>Basic Input/Output System</td>
</tr>
<tr>
<td>CMOS</td>
<td>Complementary Metal-oxide-semiconductor</td>
</tr>
<tr>
<td>CPU</td>
<td>Central Processing Unit</td>
</tr>
<tr>
<td>DDR4</td>
<td>Double Data Rate 4th edition</td>
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<tr>
<td>DIMM</td>
<td>Dual In-line Memory Module</td>
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<td>DPC</td>
<td>DIMMs per Channel</td>
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<tr>
<td>FP</td>
<td>Front Panel</td>
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<tr>
<td>FRB</td>
<td>Fault Resilient Boot</td>
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<tr>
<td>FRU</td>
<td>Field Replaceable Unit</td>
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<tr>
<td>GPGPU</td>
<td>General Purpose Graphic Processing Unit</td>
</tr>
<tr>
<td>GPIO</td>
<td>General Purpose Input/Output</td>
</tr>
<tr>
<td>GUI</td>
<td>Graphical User Interface</td>
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<tr>
<td>I²C</td>
<td>Inter-integrated Circuit bus</td>
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<tr>
<td>IMC</td>
<td>Integrated Memory Controller</td>
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<tr>
<td>IPC</td>
<td>Intel Product Code</td>
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<tr>
<td>IPMI</td>
<td>Intelligent Platform Management Interface</td>
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<tr>
<td>ISTA</td>
<td>International Safe Transit Association</td>
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<tr>
<td>LED</td>
<td>Light Emitting Diode</td>
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<tr>
<td>LFM</td>
<td>Linear Feet per Minute – Airflow measurement</td>
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<td>LPC</td>
<td>Low-pin Count</td>
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<tr>
<td>LRDIMM</td>
<td>Load Reduced DIMM</td>
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<tr>
<td>LSB</td>
<td>Least Significant Bit</td>
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<tr>
<td>MSB</td>
<td>Most Significant Bit</td>
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<tr>
<td>MRC</td>
<td>Memory Reference Code</td>
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<tr>
<td>MTBF</td>
<td>Mean Time Between Failure</td>
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<tr>
<td>NAT</td>
<td>Network Address Translation</td>
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<tr>
<td>NIC</td>
<td>Network Interface Controller</td>
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<tr>
<td>NMI</td>
<td>Non-maskable Interrupt</td>
</tr>
<tr>
<td>NTB</td>
<td>Non-Transparent Bridge</td>
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<tr>
<td>OCuLink</td>
<td>Optical Copper Link</td>
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<tr>
<td>OEM</td>
<td>Original Equipment Manufacturer</td>
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<tr>
<td>OCP*</td>
<td>Open Compute Project*</td>
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<tr>
<td>OR</td>
<td>Oct Rank</td>
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<tr>
<td>PCH</td>
<td>Peripheral Controller Hub</td>
</tr>
<tr>
<td>PCI</td>
<td>Peripheral Component Interconnect</td>
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<tr>
<td>PCB</td>
<td>Printed Circuit Board</td>
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<tr>
<td>PCIe*</td>
<td>Peripheral Component Interconnect Express*</td>
</tr>
<tr>
<td>PCI-X</td>
<td>Peripheral Component Interconnect Extended</td>
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<tr>
<td>PFC</td>
<td>Power Factor Correction</td>
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<tr>
<td>PHM</td>
<td>Processor Heat sink Module</td>
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<tr>
<td>Term</td>
<td>Definition</td>
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<td>------------</td>
<td>------------------------------------------------</td>
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<tr>
<td>PMBus</td>
<td>Power Management Bus</td>
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<tr>
<td>PMM</td>
<td>Persistent Memory Module</td>
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<tr>
<td>POST</td>
<td>Power-on Self-Test</td>
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<td>PSU</td>
<td>Power Supply Unit</td>
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<td>PWM</td>
<td>Pulse Width Modulation</td>
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<tr>
<td>QR</td>
<td>Quad Rank</td>
</tr>
<tr>
<td>RAID</td>
<td>Redundant Array of Independent Disks</td>
</tr>
<tr>
<td>RAM</td>
<td>Random Access Memory</td>
</tr>
<tr>
<td>RAS</td>
<td>Reliability, Availability, and Serviceability</td>
</tr>
<tr>
<td>RCIEP</td>
<td>Root Complex Integrated Endpoint</td>
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<tr>
<td>RDIMM</td>
<td>Registered DIMM</td>
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<tr>
<td>RMCP</td>
<td>Remote Management Control Protocol</td>
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<td>ROC</td>
<td>RAID On Chip</td>
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<tr>
<td>SAS</td>
<td>Serial Attached SCSI</td>
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<td>SATA</td>
<td>Serial Advanced Technology Attachment</td>
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<tr>
<td>SEL</td>
<td>System Event Log</td>
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<tr>
<td>SCA</td>
<td>Single Connector Attachment</td>
</tr>
<tr>
<td>SCSI</td>
<td>Small Computer System Interface</td>
</tr>
<tr>
<td>SDR</td>
<td>Sensor Data Record</td>
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<tr>
<td>SFF</td>
<td>Small Form Factor</td>
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<tr>
<td>SFP</td>
<td>Small Form-factor Pluggable</td>
</tr>
<tr>
<td>SMBus</td>
<td>System Management Bus</td>
</tr>
<tr>
<td>SR</td>
<td>Single Rank</td>
</tr>
<tr>
<td>SSD</td>
<td>Solid State Device</td>
</tr>
<tr>
<td>TCG</td>
<td>Trusted Computing Group</td>
</tr>
<tr>
<td>TDP</td>
<td>Thermal Design Power</td>
</tr>
<tr>
<td>Intel® UPI</td>
<td>Intel® Ultra Path Interconnect</td>
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</tbody>
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